# MU181000A
# 12.5 GHz Synthesizer
# MU181000B
# 12.5 GHz 4 port Synthesizer
# Operation Manual

**Seventh Edition**

- **For safety and warning information, please read this manual before attempting to use the equipment.**
- **Additional safety and warning information is provided within the MP1800A Signal Quality Analyzer Installation Guide and the MT1810A 4 Slot Chassis Installation Guide. Please also refer to these documents before using the equipment.**
- **Keep this manual with the equipment.**

**ANRITSU CORPORATION**

Document No.: M-W2750AE-7.0

# Safety Symbols

To prevent the risk of personal injury or loss related to equipment malfunction, Anritsu Corporation uses the following safety symbols to indicate safety-related information. Ensure that you clearly understand the meanings of the symbols BEFORE using the equipment. Some or all of the following symbols may be used on all Anritsu equipment. In addition, there may be other labels attached to products that are not shown in the diagrams in this manual.

## Symbols used in manual

**⚠ DANGER** This indicates a very dangerous procedure that could result in serious injury or death if not performed properly.

**⚠ WARNING** This indicates a hazardous procedure that could result in serious injury or death if not performed properly.

**⚠ CAUTION** This indicates a hazardous procedure or danger that could result in light-to-severe injury, or loss related to equipment malfunction, if proper precautions are not taken.

## Safety Symbols Used on Equipment and in Manual

The following safety symbols are used inside or on the equipment near operation locations to provide information about safety items and operation precautions. Ensure that you clearly understand the meanings of the symbols and take the necessary precautions BEFORE using the equipment.

This indicates a prohibited operation. The prohibited operation is indicated symbolically in or near the barred circle.

This indicates an obligatory safety precaution. The obligatory operation is indicated symbolically in or near the circle.

This indicates a warning or caution. The contents are indicated symbolically in or near the triangle.

This indicates a note. The contents are described in the box.

These indicate that the marked part should be recycled.

MU181000A 12.5 GHz Synthesizer
MU181000B 12.5 GHz 4 port Synthesizer
Operation Manual

27 November 2006 (First Edition)
5 December 2014 (Seventh Edition)

# Equipment Certificate

Anritsu Corporation certifies that this equipment was tested before shipment using calibrated measuring instruments with direct traceability to public testing organizations recognized by national research laboratories, including the National Institute of Advanced Industrial Science and Technology, and the National Institute of Information and Communications Technology, and was found to meet the published specifications.

# Anritsu Warranty

Anritsu Corporation will repair this equipment free-of-charge if a malfunction occurs within one year after shipment due to a manufacturing fault. However, software fixes will be made in accordance with the separate Software End-User License Agreement. Moreover, Anritsu Corporation will deem this warranty void when:

- The fault is outside the scope of the warranty conditions separately described in the operation manual.
- The fault is due to mishandling, misuse, or unauthorized modification or repair of the equipment by the customer.
- The fault is due to severe usage clearly exceeding normal usage.
- The fault is due to improper or insufficient maintenance by the customer.
- The fault is due to natural disaster, including fire, wind, flooding, earthquake, lightning strike, or volcanic ash, etc.
- The fault is due to damage caused by acts of destruction, including civil disturbance, riot, or war, etc.
- The fault is due to explosion, accident, or breakdown of any other machinery, facility, or plant, etc.
- The fault is due to use of non-specified peripheral or applied equipment or parts, or consumables, etc.
- The fault is due to use of a non-specified power supply or in a non-specified installation location.
- The fault is due to use in unusual environments(Note).
- The fault is due to activities or ingress of living organisms, such as insects, spiders, fungus, pollen, or seeds.

In addition, this warranty is valid only for the original equipment purchaser. It is not transferable if the equipment is resold.

Anritsu Corporation shall assume no liability for injury or financial loss of the customer due to the use of or a failure to be able to use this equipment.

Note:
For the purpose of this Warranty, "unusual environments" means use:

- In places of direct sunlight
- In dusty places
- Outdoors
- In liquids, such as water, oil, or organic solvents, and medical fluids, or places where these liquids may adhere
- In salty air or in places where chemically active gases (sulfur dioxide, hydrogen sulfide, chlorine, ammonia, nitrogen dioxide, or hydrogen chloride, etc.) are present
- In places where high-intensity static electric charges or electromagnetic fields are present
- In places where abnormal power voltages (high or low) or instantaneous power failures occur
- In places where condensation occurs
- In the presence of lubricating oil mists
- In places at an altitude of more than 2,000 m
- In the presence of frequent vibration or mechanical shock, such as in cars, ships, or airplanes

# Anritsu Corporation Contact

In the event of this equipment malfunctions, contact an Anritsu Service and Sales office. Contact information can be found on the last page of the printed version of this manual, and is available in a separate file on the CD version.

## Notes On Export Management

This product and its manuals may require an Export License/Approval by the Government of the product's country of origin for re-export from your country.

Before re-exporting the product or manuals, please contact us to confirm whether they are export-controlled items or not.

When you dispose of export-controlled items, the products/manuals need to be broken/shredded so as not to be unlawfully used for military purpose.

# Software End-User License Agreement (EULA)

Please read this Software End-User License Agreement (hereafter this EULA) carefully before using (includes executing, copying, registering, etc.) this software (includes programs, databases, scenarios, etc., used to operate, set, etc., Anritsu electronic equipment). By reading this EULA and using this software, you are agreeing to be bound by the terms of its contents and Anritsu Corporation (hereafter Anritsu) hereby grants you the right to use this Software with the Anritsu-specified equipment (hereafter Equipment) for the purposes set out in this EULA.

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3. You are not permitted to reverse engineer this software.
4. This EULA allows you to install one copy of this Software on one piece of Equipment.

## 2. Disclaimers

To the extent not prohibited by law, in no event shall Anritsu be liable for personal injury, or any incidental, special, indirect or consequential damages whatsoever, including, without limitation, damages for loss of profits, loss of data, business interruption or any other commercial damages or losses, arising out of or related to your use or inability to use this Software.

## 3. Limitation of Liability

a. If a fault (bug) is discovered in this Software, preventing operation as described in the operation manual or specifications whether or not the customer uses this software as described in the manual, Anritsu shall at its own discretion, fix the bug, or exchange the software, or suggest a workaround, free-of-charge. However, notwithstanding the above, the following items shall be excluded from repair and warranty.
   i) If this Software is deemed to be used for purposes not described in the operation manual or specifications.
   ii) If this Software is used in conjunction with other non-Anritsu-approved software.
   iii) Recovery of lost or damaged data.
   iv) If this Software or the Equipment has been modified, repaired, or otherwise altered without Anritsu's prior approval.
   v) For any other reasons out of Anritsu's direct control and responsibility, such as but not limited to, natural disasters, software virus infections, etc.

b. Expenses incurred for transport, hotel, daily allowance, etc., for on-site repairs by Anritsu engineers necessitated by the above faults shall be borne by you.

c. The warranty period for faults listed in article 3a above covered by this EULA shall be either 6 months from the date of purchase of this Software or 30 days after the date of repair, whichever is longer.

**4. Export Restrictions**

You may not use or otherwise export or re-export directly or indirectly this Software except as authorized by Japanese and United States law. In particular, this software may not be exported or re-exported (a) into any Japanese or US embargoed countries or (b) to anyone on the Japanese or US Treasury Department's list of Specially Designated Nationals or the US Department of Commerce Denied Persons List or Entity List. By using this Software, you warrant that you are not located in any such country or on any such list. You also agree that you will not use this Software for any purposes prohibited by Japanese and US law, including, without limitation, the development, design and manufacture or production of missiles or nuclear, chemical or biological weapons of mass destruction.

**5. Termination**

Anritsu shall deem this EULA terminated if you violate any conditions described herein. This EULA shall also be terminated if the conditions herein cannot be continued for any good reason, such as violation of copyrights, patents, or other laws and ordinances.

**6. Reparations**

If Anritsu suffers any loss, financial or otherwise, due to your violation of the terms of this EULA, Anritsu shall have the right to seek proportional damages from you.

**7. Responsibility after Termination**

Upon termination of this EULA in accordance with item 5, you shall cease all use of this Software immediately and shall as directed by Anritsu either destroy or return this Software and any backup copies, full or partial, to Anritsu.

**8. Dispute Resolution**

If matters of dispute or items not covered by this EULA arise, they shall be resolved by negotiations in good faith between you and Anritsu.

**9. Court of Jurisdiction**

This EULA shall be interpreted in accordance with Japanese law and any disputes that cannot be resolved by negotiation described in Article 8 shall be settled by the Japanese courts.

# CE Conformity Marking

Anritsu affixes the CE Conformity marking on the following product(s) in accordance with the Council Directive 93/68/EEC to indicate that they conform to the EMC and LVD directive of the European Union (EU).

**CE marking**

## 1. Product Model

Plug-in Units: MU181000A 12.5 GHz Synthesizer
MU181000B 12.5 GHz 4 port Synthesizer

## 2. Applied Directive and Standards

When the MU181000A 12.5 GHz Synthesizer or MU181000B 12.5GHz 4 port Synthesizer is installed in the MP1800A or MT1810A, the applied directive and standards of this unit conform to those of the MP1800A or MT1810A main frame.

PS: About main frame
Please contact Anritsu for the latest information on the main frame types that MU181000A/B can be used with.

# C-Tick Conformity Marking

Anritsu affixes the C-Tick marking on the following product(s) in accordance with the regulation to indicate that they conform to the EMC framework of Australia/New Zealand.

**C-Tick marking**


## 1. Product Model

Plug-in Units: MU181000A 12.5 GHz Synthesizer
MU181000B 12.5 GHz 4 port Synthesizer

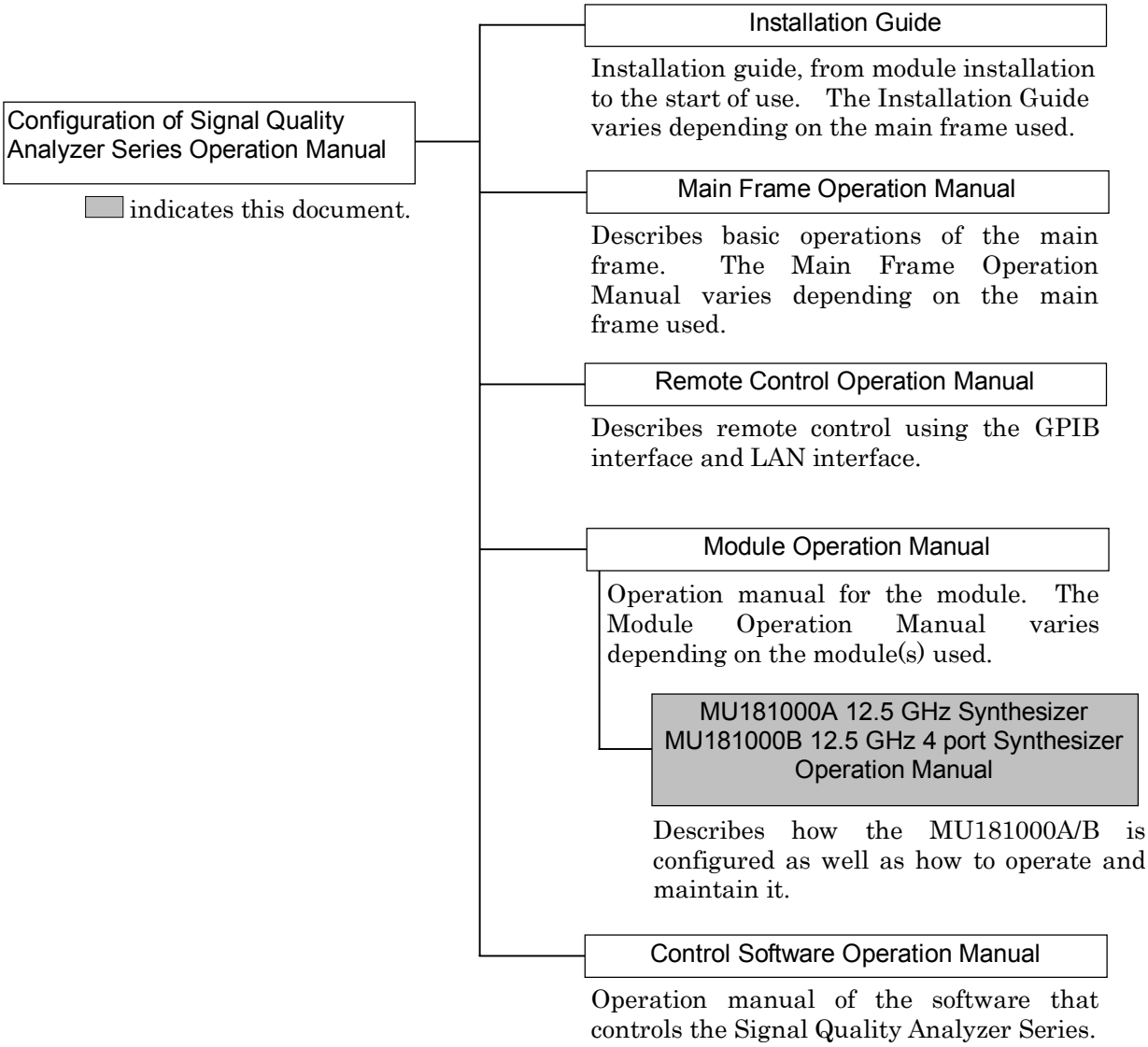
## 2. Applied Directive and Standards

When the MU181000A 12.5 GHz Synthesizer or MU181000B 12.5GHz 4 port Synthesizer is installed in the MP1800 or MT1810A, the applied directive and standards of this unit conform to those of the MP1800 or MT1810A main frame.

PS: About main frame
Please contact Anritsu for the latest information on the main frame types that MU181000A/B can be used with.

# About This Manual

A testing system combining an MP1800A Signal Quality Analyzer or MT1810A 4 Slot Chassis mainframe, module(s), and control software is called a Signal Quality Analyzer Series. The operation manuals of the Signal Quality Analyzer Series consist of separate documents for the installation guide, the mainframe, remote control operation, module(s), and control software, as shown below



# Table of Contents

*IV.*

# Chapter 1 Overview



This section provides an overview and the specifications of the MU181000A 12.5 GHz Synthesizer and the MU181000B 12.5 GHz 4 port Synthesizer (hereinafter referred to as MU181000A/B).

# 1.1 Product Overview

The MU181000A/B is a module that can be built into a Signal Quality Analyzer Series mainframe. It outputs clock signals of 100 MHz to 12.5 GHz to be input to the MU181020A 12.5 Gbit/s PPG or the MU181800A 12.5 GHz Clock Distributor.

The MU181000A/B outputs a 10-MHz reference signal to synchronize an external device with it. The MU181000A/B can also be synchronized with an external device by inputting a 10-MHz reference signal output from that device.

# 1.2 Product Composition

## 1.2.1 Standard composition

Table 1.2.1-1 and Table 1.2.1-2 shows the items standardly included with the MU181000A/B.

**Table 1.2.1-1 Standard composition of MU181000A**

| Item | Model name/symbol | Product name | Q'ty | Remarks |
|---|---|---|---|---|
| Main unit | MU181000A | 12.5 GHz Synthesizer | 1 | |
| Accessory | J1624A | Coaxial cable, 0.3 m | 1 | SMA connector |
| | Z0897A | Operation Manual | 1 | CD-ROM version |
| | Z0918A | MX180000A Software CD | 1 | CD-ROM version |

**Table 1.2.1-2 Standard composition of MU181000B**

| Item | Model name/symbol | Product name | Q'ty | Remarks |
|---|---|---|---|---|
| Main unit | MU181000B | 12.5 GHz 4 port Synthesizer | 1 | |
| Accessory | J1624A | Coaxial cable, 0.3 m | 4 | SMA connector |
| | Z0897A | Operation Manual | 1 | CD-ROM version |
| | Z0918A | MX180000A Software CD | 1 | CD-ROM version |

## 1.2.2 Option

Tables 1.2.2-1 and 1.2.2-2 show the options for the MU181000A/B. All options are sold separately.

**Table 1.2.2-1 Options for MU181000A**

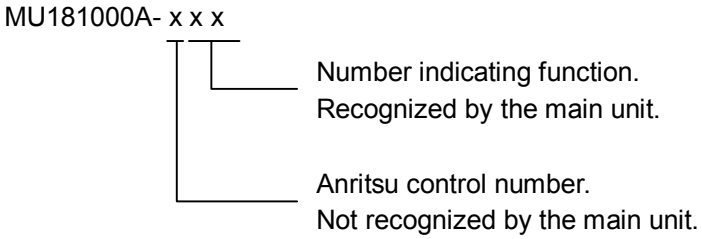
| Model name/symbol | Product name | Q'ty | Remarks |
|---|---|---|---|
| MU181000A-x01 | Jitter Modulation | 1 | |

**Table 1.2.2-2 Options for MU181000B**

| Model name/symbol | Product name | Q'ty | Remarks |
|---|---|---|---|
| MU181000B-x01 | Jitter Modulation | 1 | |

***Note:***

Option name format



## 1.2.3 Application parts

Table 1.2.3-1 shows the application parts for the MU181000A/B. All application parts are sold separately.

**Table 1.2.3-1 Application parts for MU181000A/B**

| Model name/symbol | Product | Q'ty | Remarks |
|---|---|---|---|
| J1625A | Coaxial cable, 1 m | 1 | SMA connector |
| J0127B | Coaxial cable, 2 m | 1 | SMA connector |
| J1137 | Terminator | 1 | 50 Ω SMA |
| W2750AE | Operation Manual | 1 | Printed version |

# 1.3 Specifications

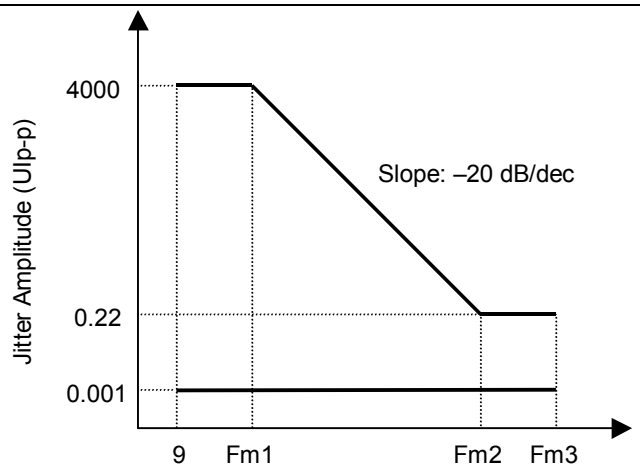
**Table 1.3-1 Specifications**

| Item | | Specifications |
|---|---|---|
| Electrical performance Clock Output | Frequency range | 0.1 to 12.5 GHz |
| | Setting resolution | 1 kHz/1 MHz switchable<br>Offset function: –1000 to +1000 ppm<br>Resolution: 1-ppm steps, 1 Hz minimum |
| | Frequency stability | ±1 ppm<br>When Reference Clock Source is set to Internal |
| | Output level | MU181000A: 0.632 to 2 Vp-p<br>MU181000B, MU181000A/B-x01: 0.4 to 1 Vp-p |
| | Phase noise | ≤–61 dBc/Hz at 1-kHz offset |
| | | ≤–80 dBc/Hz at 10-kHz offset |
| | | ≤–90 dBc/Hz at 100-kHz offset |
| | Residual wander | MU181000A/B: ≤20 ps (p-p) |
| | | MU181000A/B-x01:<br>When Fc >400 MHz: ≤20 ps (p-p)<br>When Fc ≤400 MHz: ≤0.02/Fc(Hz) ×10^–12 ps (p-p) |
| | | Measurement conditions: Overwrite for 10 s using Buff Output (when Internal is selected) as trigger signal of sampling oscilloscope.<br>Measurement points: 100 MHz/150 MHz/600 MHz/1.25 GHz/2.5 GHz/10 GHz/12.5 GHz |
| | Duty | 50 ± 10% |
| | Output wavelength | <1 GHz Rectangular wave |
| | | ≥1 GHz Sine wave or rectangular wave |
| | | Definition of rectangular wave lower than 1 GHz:<br>≤350 ps at tr, tf = 20 to 80% |
| | Clock output Channel skew | ≤10 ps (12.5 GHz)<br>Applied to the MU181000B and MU181000B- x01. |
| | Output impedance | 50 Ω/GND |
| | Connector | SMA |

**Table 1.3-1 Specifications (Cont'd)**

| Item | | Specifications |
|---|---|---|
| 10 MHz reference signal<br>Ref. Input | Frequency | 10 MHz ± 10 ppm |
| | Level | 0.5 to 2.0 Vp-p (AC) |
| | Impedance | 50 Ω/GND |
| | Waveform | Sine wave or rectangular wave |
| | Duty | 50 ± 10% |
| | Connector | BNC |
| Buff Output | Frequency stability | Internal: 10 MHz ± 1 ppm<br>External: Depends on reference input to Ref. Input |
| | Level | 1.0 Vp-p ± 30% (AC) |
| | Impedance | 50 Ω/GND |
| | Waveform | Rectangular wave |
| | Duty | 50 ± 10% |
| | Connector | BNC |
| Trigger Output<br>When MU181000A/B-x01 is installed | Effective range | 800 MHz < Fc ≤ 12.5 GHz Fc: Clock output frequency |
| | Output frequency | When 6.4 GHz < Fc ≤ 12.5 GHz: 1 or 64 division selectable<br>When 800 MHz < Fc ≤ 6.4 GHz: Fixed to 64 divisions |
| | Output level | 0.4 to 1.1 Vp-p (AC) |
| | Output impedance | 50 Ω/GND |
| | Connector | SMA |
| External modulation input (Jitter Ext Input)<br>When MU181000A/B-x01 is installed | Refer to Sections 2.2 "How to Operate Application" and 2.3 "Preventing Damage" for functions and performance. | |
| | Frequency range | 9 Hz to 1 GHz |
| | Input waveform | Sine wave |
| | Input level range | 3 Vp-p Max, 0 Vdc max. |
| | Input impedance | 50 Ω/GND |
| | Connector | SMA |

**Table 1.3-1 Specifications (Cont'd)**

<table>
<tr><th colspan="2">Item</th><th>Specifications</th></tr>
<tr><td rowspan="4">External I,Q<br>When MU181000A/B-x01 is installed</td><td>Frequency band</td><td>DC to 320 MHz max. (–3 dB)<br>Note that the maximum band is limited, depending on the set bit rate.<br>2.4 GHz < Fc ≤ 4.0 GHz: 320 MHz<br>1.4 GHz < Fc ≤ 2.4 GHz: 100 MHz<br>0.65 GHz < Fc ≤ 1.4 GHz: 20 MHz<br>0.4 GHz < Fc ≤ 0.65 GHz: 10 MHz<br>0.1 GHz ≤ Fc ≤ 0.4 GHz: 5 MHz</td></tr>
<tr><td>Input level range</td><td>±0.5 V</td></tr>
<tr><td>Input impedance</td><td>50 Ω/GND</td></tr>
<tr><td>Connector</td><td>BNC</td></tr>
<tr><td rowspan="7">100 MHz reference signal input (100 MHz Ref Input)<br>When MU181000A/B-x01 is installed</td><td rowspan="3">Input signal</td><td>Carrier frequency: 100 MHz</td></tr>
<tr><td>Modulation frequency: 30 to 33 kHz</td></tr>
<tr><td>Frequency deviation amount: 500 kHzp-p max.</td></tr>
<tr><td>Level</td><td>1 Vp-p ± 30% (AC)</td></tr>
<tr><td>Waveform</td><td>Sine wave or rectangular wave</td></tr>
<tr><td>Duty</td><td>50 ± 10%</td></tr>
<tr><td>Input impedance</td><td>50 Ω/GND</td></tr>
<tr><td>Connector</td><td>BNC</td></tr>
<tr><td>Modulation function (Internal Jitter)<br>When MU181000A/B-x01 is installed</td><td>Jitter mask</td><td> 

<table>
<tr><th>Fc range (GHz)</th><th>Fm1(Hz)</th><th>Fm2(Hz)</th><th>Fm3(Hz)</th></tr>
<tr><td>6.4 < Fc ≤ 12.5</td><td>220</td><td>4 M</td><td>80 M</td></tr>
<tr><td>3.2 < Fc ≤ 6.4</td><td>110</td><td>2 M</td><td>40 M</td></tr>
<tr><td>1.6 < Fc ≤ 3.2</td><td>55</td><td>1 M</td><td>20 M</td></tr>
<tr><td>0.8 < Fc ≤ 1.6</td><td>27.5</td><td>500 k</td><td>10 M</td></tr>
<tr><td>0.1 ≤ Fc ≤ 0.8</td><td>13.75</td><td>250 k</td><td>5 M</td></tr>
</table></td></tr>
</table>

**Table 1.3-1 Specifications (Cont'd)**

| Item | | Specifications |
|---|---|---|
| Modulation function (Internal Jitter) (Cont'd) When MU181000A/B-x01 is installed | Modulation frequency range | 6.4 GHz < Fc ≤ 12.5 GHz: 9 Hz to 80 MHz |
| | | 3.2 GHz < Fc ≤ 6.4 GHz : 9 Hz to 40 MHz |
| | | 1.6 GHz < Fc ≤ 3.2 GHz : 9 Hz to 20 MHz |
| | | 0.8 GHz < Fc ≤ 1.6 GHz: 9 Hz to 10 MHz |
| | | 0.1 GHz ≤ Fc ≤ 0.8 GHz: 9 Hz to 5 MHz |
| | Modulation frequency setting resolution | 9 Hz ≤ Fm ≤ 10 Hz: 0.001-Hz steps |
| | | 10 Hz < Fm ≤ 100 Hz: 0.01-Hz steps |
| | | 100 Hz < Fm ≤ 1 kHz: 0.1-Hz steps |
| | | 1 kHz < Fm ≤ 10 kHz: 1-Hz steps |
| | | 10 kHz < Fm ≤ 100 kHz: 10-Hz steps |
| | | 100 kHz < Fm ≤ 1 MHz: 100-Hz steps |
| | | 1 MHz < Fm ≤ 10 MHz: 1-kHz steps |
| | | 10 MHz < Fm ≤ 80 MHz: 10-kHz steps |
| | Modulation frequency accuracy | ±100 ppm |
| | Jitter amplitude Setting resolution | 0.000 to 0.999 UIp-p: 0.001-UI steps |
| | | 1.00 to 32.00 UIp-p: 0.01-UI steps |
| | | 32.1 to 256.0 UIp-p: 0.1-UI steps |
| | | 257 to 2049 UIp-p: 1-UI steps |
| | | 2050 to 4000 UIp-p: 10-UI steps |
| | Jitter amplitude accuracy | Fc < 1 GHz<br>0.001 to 2.19 UIp-p: ±0.01 UI ± Q%<br>2.2 to 21.99 UIp-p: ±0.2 UI ± Q%<br>22 to 4000 UIp-p: ±2 UI ± Q%<br>Fc ≥ 1 GHz<br>0.001 to 2.19 UIp-p: ±0.02 UI ± Q%<br>2.2 to 21.99 UIp-p: ±0.2 UI ± Q%<br>22 to 4000 UIp-p: ±2 UI ± Q%<br><br>Fm (Hz) / Q<br>9 ≤ Fm ≤ 500 k / 7<br>500 k < Fm ≤ 2 M / 12<br>2 M < Fm ≤ 80 M / 15 |

**Table 1.3-1 Specifications (Cont'd)**

<table>
<tr><th colspan="2">Item</th><th>Specifications</th></tr>
<tr><td rowspan="4">External Jitter 1<br>When MU181000A/B-x01 is installed</td><td>Modulation frequency range</td><td>When Operation is Variable;<br>4.0 GHz ≤ Fc ≤ 12.5 GHz: 9 Hz to 1 GHz<br>2.4 GHz < Fc < 4.0 GHz: 9Hz to 500 MHz<br>1.4 GHz < Fc ≤ 2.4 GHz: 9 Hz to 100 MHz<br>0.65 GHz < Fc ≤ 1.4 GHz: 9 Hz to 20 MHz<br>0.4 GHz < Fc ≤ 0.65 GHz: 9 Hz to 10 MHz<br>0.1 GHz ≤ Fc ≤ 0.4 GHz: 9 Hz to 5 MHz</td></tr>
<tr><td>Input waveform</td><td>Sine wave</td></tr>
<tr><td>FM frequency range</td><td>275 Hz/550 Hz/1 kHz/1.1 kHz/2.2 kHz/2.75 kHz/4.4 kHz/<br>5.5 kHz/11 kHz/22 kHz/27.5 kHz/44 kHz/55 kHz/<br>100 kHz/110 kHz/220 kHz/250 kHz/440 kHz/500 kHz/<br>1 MHz/2 MHz/4 MHz/80 MHz/500 MHz/1 GHz/Full<br>Full Range is supported when UI Range is 0.22 UI while Input Freq. is 4 MHz or more.</td></tr>
<tr><td>Jitter amplitude range</td><td>0.22 UI/2.0 UI/20 UI/200 UI/4000 UI</td></tr>
<tr><td>Modulation sensitivity</td><td>For 0.22 UI Range<br>When input level is 0.5 Vp-p:
<table>
<tr><th>FM Frequency Range</th><th>Input Frequency</th><th>Jitter Amplitude</th></tr>
<tr><td>4 MHz</td><td>9 Hz to 4 MHz</td><td rowspan="4">0.1 UIp-p± 0.03 UI</td></tr>
<tr><td>80 MHz</td><td>4 to 80 MHz</td></tr>
<tr><td>500 MHz*1</td><td>80 to 500 MHz</td></tr>
<tr><td>1 GHz*2</td><td>500 MHz to 1 GHz</td></tr>
</table>
*1: 500 MHz Range is not applied at 0.1 G≤Fc≤1.4 GHz.<br>*2: 1 GHz Range is not applied at 2.4 G≤Fc≤4 GHz.<br>The upper limit modulation frequency is listed above.<br><br>When the input level at 0.1 UIp-p is Vin, and jitter amplitude is UIx:<br>Relationship between Vin and UIx is approximated as follows.<br>Unit of Vin is Vp-p while UIp-p for UIx<br>When UIx ≤ 0.1 UIp-p:<br>UIx = 0.2 × Vin<br>Vin = UIx/0.2<br>When 0.1 UIp-p < UIx ≤ 0.22 UIp-p:<br>UIx = 0.2 × Vin × (1 – 0.22 × (Vin – 0.5))<br>Vin = 2.5 – root (6.25 – 22.73 × UIx)</td></tr>
</table>

**Table 1.3-1 Specifications (Cont'd)**

<table>
<tr><th colspan="2">Item</th><th>Specifications</th></tr>
<tr><td>External Jitter 1 (Cont'd)<br>When MU181000A/B-x01 is installed</td><td>Modulation sensitivity (Cont'd)</td><td>
For 2 UI/20 UI/200 UI/4000 UI Range<br>
When input level is 0.5 Vp-p:<br>
Clock output frequency: 6.4 GHz < Fc ≤ 12.5 GHz
<table>
<tr><th>Jitter Amplitude Range</th><th>FM Frequency Range</th><th>Input Frequency</th><th>Jitter Amplitude</th></tr>
<tr><td>2 UI</td><td>4 MHz</td><td>440 kHz</td><td>1 UIp-p±0.3 UI</td></tr>
<tr><td>20 UI</td><td>440 kHz</td><td>44 kHz</td><td>10 UIp-p±3 UI</td></tr>
<tr><td>200 UI</td><td>44 kHz</td><td>4.4 kHz</td><td>100 UIp-p±30 UI</td></tr>
<tr><td>4000 UI</td><td>4.4 kHz</td><td>220 Hz</td><td>1000 UIp-p±300 UI</td></tr>
</table>
Clock output frequency: 3.2 GHz < Fc ≤ 6.4 GHz
<table>
<tr><th>Jitter Amplitude Range</th><th>FM Frequency Range</th><th>Input Frequency</th><th>Jitter Amplitude</th></tr>
<tr><td>2 UI</td><td>2 MHz</td><td>220 kHz</td><td>1 UIp-p±0.3 UI</td></tr>
<tr><td>20 UI</td><td>220 kHz</td><td>22 kHz</td><td>10 UIp-p±3 UI</td></tr>
<tr><td>200 UI</td><td>22 kHz</td><td>2.2 kHz</td><td>100 UIp-p±30 UI</td></tr>
<tr><td>4000 UI</td><td>2.2 kHz</td><td>110 Hz</td><td>1000 UIp-p±300 UI</td></tr>
</table>
Clock output frequency: 1.6 GHz < Fc ≤ 3.2 GHz
<table>
<tr><th>Jitter Amplitude Range</th><th>FM Frequency Range</th><th>Input Frequency</th><th>Jitter Amplitude</th></tr>
<tr><td>2 UI</td><td>1 MHz</td><td>110 kHz</td><td>1 UIp-p±0.3 UI</td></tr>
<tr><td>20 UI</td><td>110 kHz</td><td>11 kHz</td><td>10 UIp-p±3 UI</td></tr>
<tr><td>200 UI</td><td>11 kHz</td><td>1.1 kHz</td><td>100 UIp-p±30 UI</td></tr>
<tr><td>4000 UI</td><td>1.1 kHz</td><td>55 Hz</td><td>1000 UIp-p±300 UI</td></tr>
</table>
Clock output frequency: 0.8 GHz < Fc ≤ 1.6 GHz
<table>
<tr><th>Jitter Amplitude Range</th><th>FM Frequency Range</th><th>Input Frequency</th><th>Jitter Amplitude</th></tr>
<tr><td>2 UI</td><td>500 kHz</td><td>55 kHz</td><td>1 UIp-p±0.3 UI</td></tr>
<tr><td>20 UI</td><td>55 kHz</td><td>5.5 kHz</td><td>10 UIp-p±3 UI</td></tr>
<tr><td>200 UI</td><td>5.5 kHz</td><td>550 Hz</td><td>100 UIp-p±30 UI</td></tr>
<tr><td>4000 UI</td><td>550 Hz</td><td>27.5 Hz</td><td>1000 UIp-p±300 UI</td></tr>
</table>
Clock output frequency: 0.1 GHz < Fc ≤ 0.8 GHz
<table>
<tr><th>Jitter Amplitude Range</th><th>FM Frequency Range</th><th>Input Frequency</th><th>Jitter Amplitude</th></tr>
<tr><td>2 UI</td><td>250 kHz</td><td>27.5 kHz</td><td>1 UIp-p±0.3 UI</td></tr>
<tr><td>20 UI</td><td>27.5 kHz</td><td>2.75 kHz</td><td>10 UIp-p±3 UI</td></tr>
<tr><td>200 UI</td><td>2.75 kHz</td><td>275 Hz</td><td>100 UIp-p±30 UI</td></tr>
<tr><td>4000 UI</td><td>275 Hz</td><td>13.75 Hz</td><td>1000 UIp-p±300 UI</td></tr>
</table>
Jitter amplitude range<br>
2 UI Range: 0.22 to 2 UIp-p (0.11 to 1 Vp-p)<br>
20 UI Range: 2 to 20 UIp-p (0.1 to 1 Vp-p)<br>
200 UI Range: 20 to 200 UIp-p (0.1 to 1 Vp-p)<br>
4000 UI Range: 200 to 4000 UIp-p (0.1 to 2 Vp-p)
</td></tr>
</table>

**Table 1.3-1 Specifications (Cont'd)**

<table>
<tr><th colspan="2">Item</th><th>Specifications</th></tr>
<tr><td>External Jitter 1 (Cont'd)<br>When MU181000A/B-x01 is installed:</td><td>Jitter Mask</td><td>When FM Freq.Range is 500 MHz/1 GHz :
<table>
<tr><th>Fc [GHz]</th><th>FM Frequency [Hz]</th><th>Jitter Amplitude [UIp-p] (Max.)</th></tr>
<tr><td rowspan="2">11.3＜Fm≦12.5</td><td>500 M～1 G</td><td>0.1</td></tr>
<tr><td>80 M～500 M</td><td>0.22</td></tr>
<tr><td>4.0＜Fm≦11.3</td><td>80M～1 G</td><td>0.22</td></tr>
<tr><td>2.4＜Fm≦4.0</td><td>80 M～500 M</td><td>0.22</td></tr>
<tr><td>1.4＜Fm≦2.4</td><td>80 M～100 M</td><td>0.22</td></tr>
</table></td></tr>
<tr><td rowspan="3">External Jitter 2<br>When MU181000A/B-x01 is installed:</td><td colspan="2">When Operation is S-ATA (clock output frequency is fixed to 6 GHz)</td></tr>
<tr><td>Modulation frequency</td><td>600 MHz</td></tr>
<tr><td>Modulation accuracy</td><td>0.455 UIp-p ± 0.091 UI when input level is 2 Vp-p</td></tr>
<tr><td rowspan="7">Triangle Wave Modulation<br>When MU181000A/B-x01 is installed:</td><td colspan="2">When Operation is S-ATA (clock output frequency is fixed to 6 GHz)</td></tr>
<tr><td>Modulation frequency</td><td>600 MHz</td></tr>
<tr><td>Modulation accuracy</td><td>0.455 Uip-p ± 0.091 UI when input level is 2 Vp-p</td></tr>
<tr><td colspan="2">When Operation is PCIe-GenI (2.5 GHz) or PCIe-GenII (5 GHz)</td></tr>
<tr><td>Clock output frequency</td><td>PCIe-GenI (2.5 GHz)<br>When Spread Method is Center: 2500 MHz<br>When Spread Method is Down: 2493.75 MHz<br>PCIe-GenII (5 GHz)<br>When Spread Method is Center: 5000 MHz<br>When Spread Method is Down: 4987.5 MHz</td></tr>
<tr><td>Modulation frequency accuracy</td><td>Offset function –1000 to +1000 ppm/1-ppm steps effective</td></tr>
<tr><td>Frequency deviation</td><td>31.25 kHz ± 1000 ppm<br>PCIe-GenI (2.5 GHz): ±6.25 MHz<br>PCIe-GenII (5 GHz): ±12.5 MHz</td></tr>
<tr><td>Deviation accuracy</td><td>±10%</td></tr>
<tr><td colspan="2">Alarm detection function</td><td>PLL unlock<br>Abnormal temperature detection (78.5℃)</td></tr>
</table>

**Table 1.3-1 Specifications (Cont'd)**

| Item | | Specifications |
|---|---|---|
| Environmental performance When MU181000A/B-x01 is installed: | Operating temperature range | +5 to +40°C (Main unit ambient temperature) |
| | Operating humidity range | 20 to 80% |
| | Storage temperature range | –20 to +60°C (recommended storage temperature range: +5 to +30°C) |
| | Storage humidity range | 20 to 80% (recommended storage humidity range: 40 to 75%) |
| Mechanical dimensions | Dimensions | 234 mm (W) × 41 mm (H) × 175 mm (D) (Compact-PCI 2 slots), excluding protruding parts |
| | Mass | ≤3.0 kg |

# Section 2 Preparation before Use

This section describes preparations required before using the MU181000A/B.

# 2.1 Installation to Signal Quality Analyzer

For information on how to install the MU181000A/B to the signal quality analyzer and how to turn on the power, refer to Section 2 “Preparation before Use” in the Signal Quality Analyzer Series Installation Guide.

# 2.2 How to Operate Application

The modules connected to the signal quality analyzer are controlled by operating the MX180000A Signal Quality Analyzer Control Software (hereinafter, referred to as “MX180000A”).

For information on how to start up, shut down, and operate MX180000A, refer to the MX180000A Signal Quality Analyzer Control Software Operation Manual.

# 2.3 Preventing Damage

Be sure to observe the rating voltage ranges when connecting input and output of the MU181000A/B. Otherwise, the MU181000A/B may become damaged.

## ⚠ CAUTION

1. **When signals are input to the MU181000A/B, avoid excessive voltage beyond the rating. Otherwise, the circuit may be damaged.**

2. **Use a 50 Ω/GND terminator at the output. Never feed any current to the output.**

3. **As a countermeasure against static electricity, ground other devices to be connected (including experimental circuits) with ground wires before connecting the I/O connector.**

4. **The outer conductor and core of the coaxial cable may become charged as a capacitor. Use metal like a copper wire to discharge electricity between the outer conductor and core before use.**

5. **Never open the MU181000A/B. If you open it and sufficient performance cannot be obtained, we may decline to repair the MU181000A/B.**

6. **To protect the MU181000A/B from electrostatic discharge failure, a conductive sheet should be placed onto the workbench, and the operator should wear an electrostatic discharge wrist strap. Connect the ground connection end of the wrist strap to the conductive sheet or to the ground terminal of the mainframe.**

# Section 3 Panel Layout and Connectors

This section describes the panels and connectors of the MU181000A/B.

# 3.1 Panel Layout

## 3.1.1 Panel layout of MU181000A/B

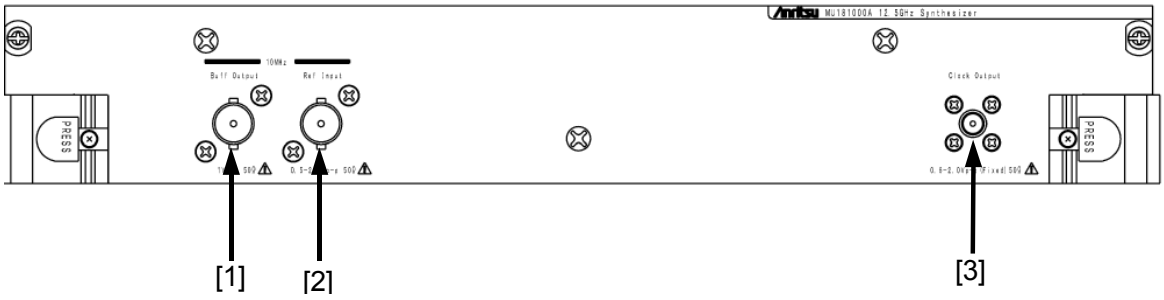



**Fig. 3.1.1-1 Panel of MU181000A**


**Fig. 3.1.1-2 Panel of MU181000B**

**Table 3.1.1-1 Name and Function of Each Part**

| No. | Name | Function |
|---|---|---|
| [1] | Buff Output<br>(10 MHz) | Connector to output 10-MHz buffer. |
| [2] | Ref. Input<br>(10 MHz) | Connector to input a 10-MHz reference input signal. Clock output of the MU181000A/B is synchronized with the clock input from this connector. |
| [3] | Clock Output<br>(100 MHz to 12.5 GHz) | Connector to output a clock signal generated within the MU181000A/B. |

## 3.1.2 Panel layout of MU181000A/B-x01

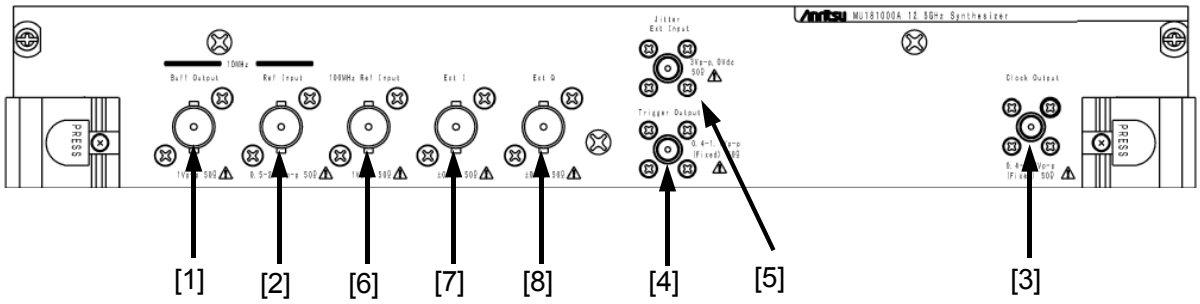



**Fig. 3.1.2-1 Panel of MU181000A-x01**

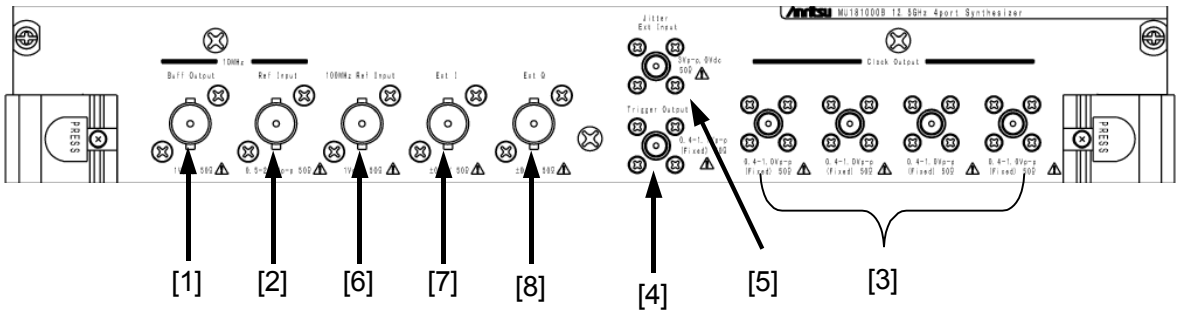



**Fig. 3.1.2-2 Panel of MU181000B-x01**

**Table 3.1.2-1 Name and Function of Each Part**

<table>
<tr><th>No.</th><th>Name</th><th>Function</th></tr>
<tr><td>[1]</td><td>Buff Output<br>(10 MHz)</td><td>Connector to output 10-MHz buffer.</td></tr>
<tr><td>[2]</td><td>Ref. Input<br>(10 MHz)</td><td>Connector to input a 10-MHz reference input signal. Clock output of the MU181000A/B is synchronized with the reference clock input from this connector.</td></tr>
<tr><td>[3]</td><td>Clock Output<br>(100 MHz～12.5 GHz)</td><td>Connector to output a clock signal generated within the MU181000A/B.</td></tr>
<tr><td>[4]</td><td>Trigger Output</td><td>Connector to output a signal generated by dividing the clock signal output frequency by 1 or 64.<br>No signal is output if the clock output frequency is 800 MHz or less.<br>An unmodulated signal or a signal with jitter added is output according to the following jitter setting.
<table>
<tr><th>Jitter Source</th><th>Jitter Amplitude</th><th>Output Signal</th></tr>
<tr><td rowspan="2">Internal</td><td>≤0.22 UIpp</td><td>Unmodulated</td></tr>
<tr><td>>0.22 UIpp</td><td>Jitter added</td></tr>
<tr><td rowspan="2">External</td><td>Range: 0.22 UI/Full</td><td>Unmodulated</td></tr>
<tr><td>Range: 2 UI/20 UI/200 UI/400 UI</td><td>Jitter added</td></tr>
</table></td></tr>
<tr><td>[5]</td><td>Jitter Ext Input</td><td>Connector to supply the modulation signal source externally.<br>A sine wave from 9 Hz to 1 GHz can be input.<br>The jitter amplitude can be controlled by the signal amplitude and by setting Amplitude Range on the screen.</td></tr>
<tr><td>[6]</td><td>100 MHz Ref Input</td><td>Connector to input a 100-MHz reference signal.<br>A clock signal is generated from this reference signal by multiplying the frequency and the phase deviation of the signal by 25 or 50, and then the generated clock signal is output from the clock signal output connector.</td></tr>
<tr><td>[7]</td><td>Ext I</td><td>Connector to input the I signal.</td></tr>
<tr><td>[8]</td><td>Ext Q</td><td>Connector to input the Q signal.</td></tr>
</table>

# 3.2 Inter-Module Connection

This section shows an example of connecting the MU181000A/B, MU181020A 12.5 Gbit/s PPG (hereinafter, referred to as "MU181020A"), and MU181800A 12.5 GHz Clock Distributor (hereinafter, referred to as "MU181800A") that are inserted into a mainframe. Connect these modules using the procedure below and referring to Figs. 3.2-1 through 3.2-3. In this section, drawings of the MU181000A are used for explanation, while the contents are common to the MU181000B.

***Note:***

Avoid static electricity when handling the devices.

1. Connecting power supply
   Connect the 3-pin power cord of the mainframe to the power receptacle. Be sure to use the 3-pin power cord supplied with the mainframe and a 3-pin receptacle.
2. Connecting to MU181020A
   Connect the Clock Output connector of the MU181000A/B and the Ext. Clock Input connector of the MU181020A, using a coaxial cable.

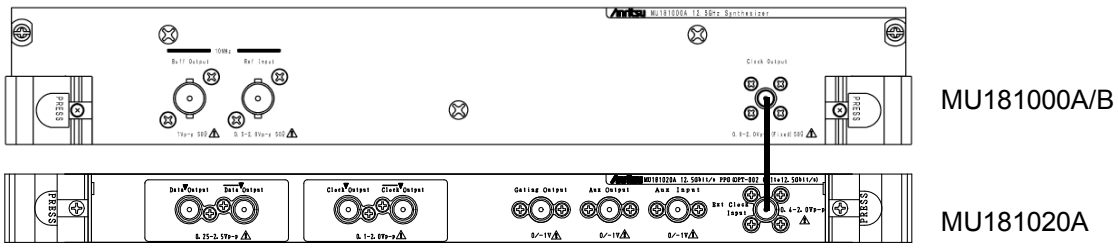



**Fig. 3.2-1 Connecting to MU181020A**

3. Connecting to MU181800A
   Connect the Clock Output connector of the MU181000A/B and the Clock Input connector of the MU181800A, using a coaxial cable.

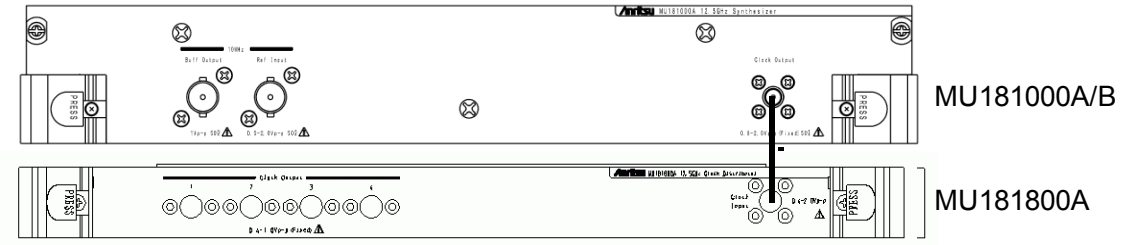



**Fig. 3.2-2 Connecting to MU181800A**

4. Connecting to external device
   Connect the Ref. Input connector of the MU181000A/B and the Ref. Output connector of an external device, using a coaxial cable.

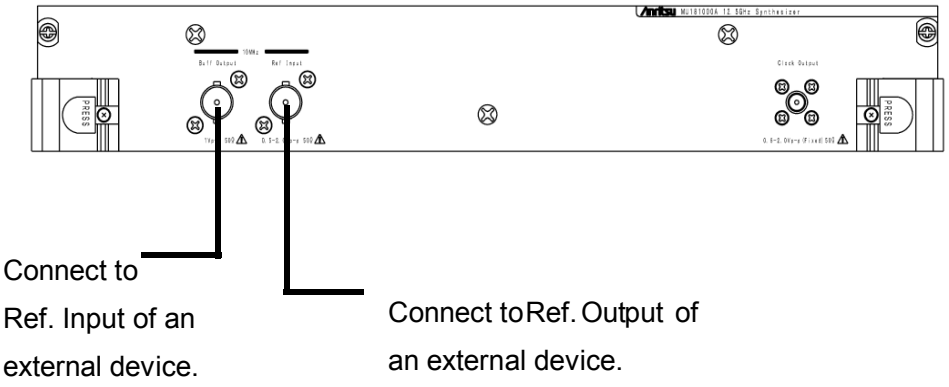



**Fig. 3.2-3 Connecting to external device**

5. Connect the MU181000A/B with MU181000A/B-x01 installed and an external device in accordance with the intended use. The connection method is as follows.
   (1) To synchronize the MU181000A/B with a 10-MHz reference signal of an external device, connect the Ref. Input connector of the MU181000A/B and the Ref. Output connector of an external device, using a 50-Ω coaxial cable.
   (2) To synchronize an external device to the 10-MHz reference signal of the MU181000A/B, connect the Buff. Output connector of the MU181000A/B and the Ref. Input connector of an external device, using a 50-Ω coaxial cable.
   (3) To use the trigger output signal (output clock frequency divided by 1 or 64) as a trigger of an oscilloscope, connect the Trigger Output connector of the MU181000A/B to the trigger input of the oscilloscope, using a 50-Ω coaxial cable.
   (4) To add jitters using an external signal source, connect the signal source to the Jitter Ext Input connector of the MU181000A/B, using a 50-Ω coaxial cable.
   (5) To output a clock signal obtained by multiplying the frequency of the 100-MHz signal input from an external signal source by 25 or 50, connect the signal source to the 100 MHz Ref Input connector of the MU181000A/B, using a 50-Ω coaxial cable.
   (6) To add jitters using an external arbitral waveform signal generator, connect the signal generator to the Ext I and Ext Q connectors of the MU181000A/B, using a 50-Ω coaxial cable.

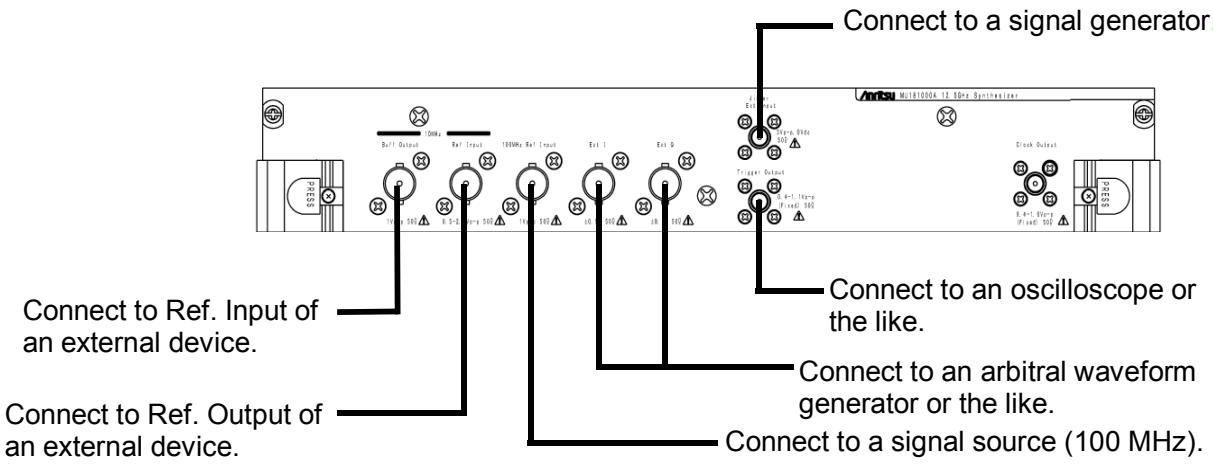



**Fig. 3.2-4 Connecting to external device (when MU181000A-x01 installed)**

## ⚠ CAUTION

**If an excessive voltage is applied to the input connector, the protective circuit may be damaged. Avoid any input beyond the rating. If there is any possibility of the rating being exceeded, check that the input signal is within the rating before connection.**

**To prevent damage due to static electricity charged inside the coaxial cable, ground the core of the coaxial cable in contact to discharge it before connection.**

# Section 4 Configuration of Setup Dialog Box

This section describes the configuration of the setup dialog box for the MU181000A/B.

# 4.1 Configuration of Entire Setup Dialog Box

The configuration of the setup dialog box when the MU181000A/B is inserted into a mainframe is shown below.

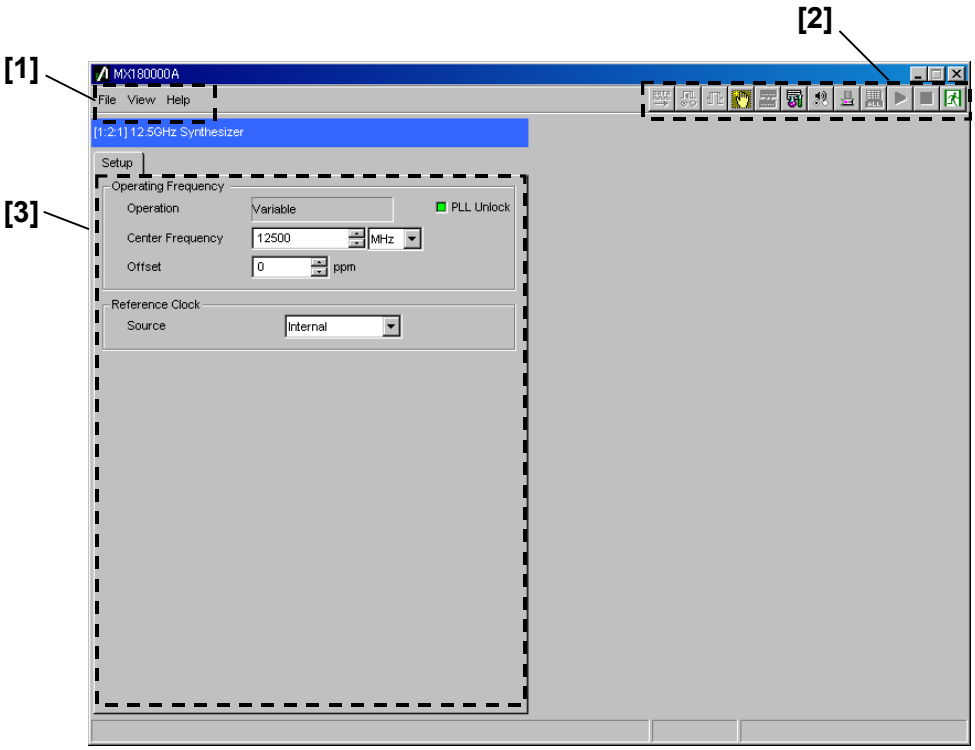



**Fig. 4.1-1 Configuration of entire setup dialog box for MU181000A/B**

The setup dialog box consists of three blocks as shown in Fig. 4.1-1. The following table describes each of the blocks.

**Table 4.1-1 Functions of blocks**

| No. | Block | Function |
|---|---|---|
| [1] | Menu bar | Selects the settings related to the entire device.<br>Refer to the MX180000A Signal Quality Analyzer Control Software Operation Manual for details. |
| [2] | Module function buttons | Shortcut buttons for the function items specific to the displayed module. Users can customize the pre-defined function buttons according to their own applications.<br>Refer to the MX180000A Signal Quality Analyzer Control Software Operation Manual for details. |
| [3] | Operation window | Configures settings specific to each module. |

# 4.2 Configuration of Operation Window

## 4.2.1 Operation window for MU181000A/B

The operation window for the MU181000A/B is shown below.

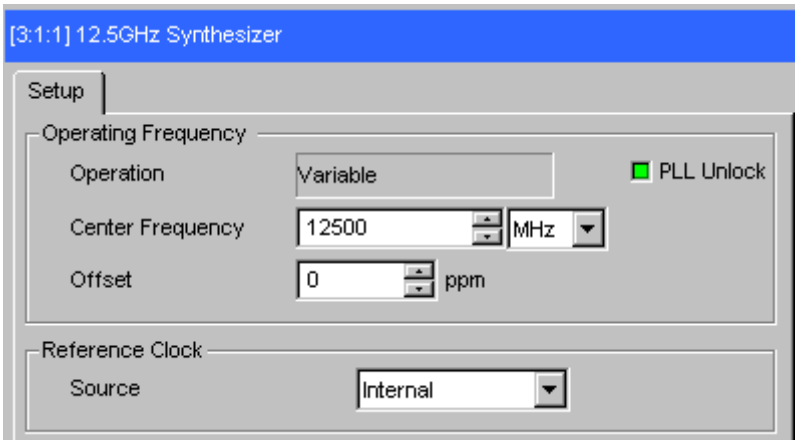



**Fig. 4.2.1-1 Operation window**

**Table 4.2.1-1 Items in operation window**

| Item | | Functions |
|---|---|---|
| Operating Frequency | Center Frequency | Sets the frequency of the clock signal output from the Clock Output connector (when Offset = 0 ppm).<br>In MHz units: Can be set in the range from 100 to 12,500 MHz. Digits below MHz that are not displayed are set to 0.<br>In kHz units: Can be set in the range from 100,000 to 12,500,000 kHz. |
| | Offset | Sets the offset value from Center Frequency for the frequency of the clock signal output from the Clock Output connector. Unit: ppm<br>Setting range: –1,000 to +1,000 ppm, in 1 ppm steps |
| | PLL Unlock | Green: PLL lock state<br>Red: PLL unlock state |
| Reference Clock | Source | Internal: Synchronizes the clock signal output from the Clock Output connector with the 10-MHz reference signal in the main frame.<br>External 10 MHz: Synchronizes the clock signal output from the Clock Output connector with the 10-MHz clock signal input to the Ref. Input (10 MHz) connector from an external device. |

## 4.2.2 Operation window when MU181000A/B-x01 is installed

The operation window for the MU181000A/B with MU181000A/B-x01 installed is shown below.

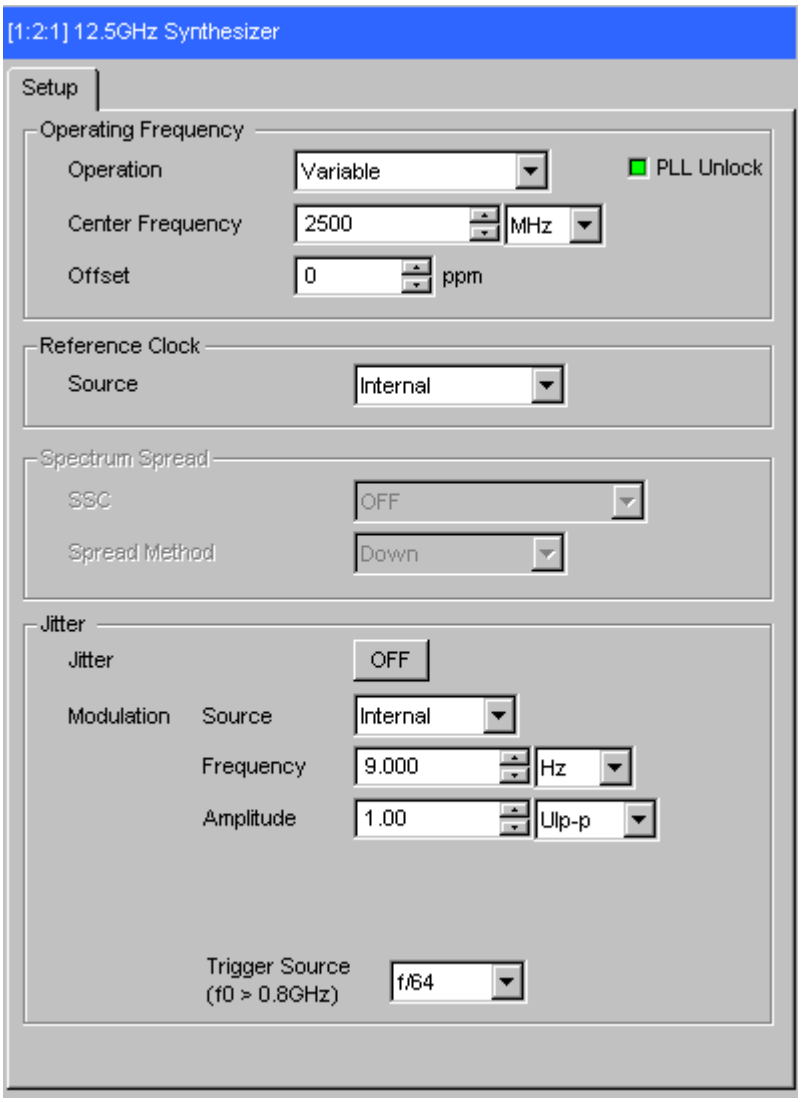



**Fig. 4.2.2-1 Operation window MU181000A/B-x01 installed**

**Table 4.2.2-1 Items in operation window when MU181000A/B-x01 is installed**

| Item | | Functions |
|---|---|---|
| Operating Frequency | Operation | Select Variable, PCIe-GenI (2.5 GHz), PCIe-GenII (5 GHz), or SATA (6 GHz). |
| | Center Frequency | When Operation is set to Variable:<br>Sets the frequency of the clock signal output from the Clock Output connector (when Offset = 0 ppm).<br>In MHz units: Can be set in the range from 100 to 12,500 MHz. Digits below MHz that are not displayed are set to 0.<br>In kHz units: Can be set in the range from 100,000 to 12,500,000 kHz.<br>When Operation is set to PCIe-GenI (2.5 GHz):<br>Fixed to 2500 MHz.<br>When Operation is set to PCIe-GenII (5 GHz):<br>Fixed to 5000 MHz.<br>When Operation is set to SATA (6 GHz):<br>Fixed to 6000 MHz. |
| | Offset | Sets the offset value from Center Frequency for the frequency of the clock signal output from the Clock Output connector. Unit: ppm<br>Setting range: –1,000 to +1,000 ppm, in 1 ppm steps<br>This item is disabled when Operation is set to PCIe-GenI (2.5 GHz) or PCIe-GenII (5 GHz) and SSC is set to ON (Ext Ref 100 MHz). |
| | PLL Unlock | Green: PLL lock state<br>Red: PLL unlock state |
| Reference Clock | Source | Internal: Synchronizes the clock signal output from the Clock Output connector with the 10-MHz reference signal in the main frame.<br>External 10 MHz: Synchronizes the clock signal output from the Clock Output connector with the 10-MHz clock signal input to the Ref. Input (10 MHz) connector from an external device.<br>This item is disabled when SSC is set to ON Int Ref, ON (Ext Ref 10 MHz), or ON (Ext Ref 100 MHz). |

**Table 4.2.2-1 Items in operation window when MU181000A/B-x01 is installed (Cont'd)**

| Item | | Functions |
|---|---|---|
| Spectrum Spread | SSC | This item is enabled only when Operation is set to PCIe-GenI (2.5 GHz) or PCIe-GenII (5 GHz).<br>OFF: An unmodulated clock signal is output.<br>ON (Int Ref): A triangular wave modulated clock signal is output. The carrier is synchronized with the internal 10-MHz reference signal.<br>ON (Ext Ref 10 MHz): A triangular wave modulated clock signal is output. The carrier is synchronized with an externally input 10-MHz clock signal.<br>ON (Ext Ref 100 MHz): A clock signal that is obtained by multiplying the frequency of the 100-MHz signal input from the 100 MHz Ref Input connector by 25 or 50 is output. |
| | Spread Method | This item is enabled only when SSC is set to OFF, ON (Int Ref), or ON (Ext Ref 10 MHz).<br>Down: When Operation is set to PCIe-GenI (2.5 GHz), a 2493.75-MHz signal is output, synchronized with the selected 10-MHz reference signal.<br>When Operation is set to PCIe-GenII (5 GHz), a 4987.5-MHz signal is output, synchronized with the selected 10-MHz reference signal.<br>Center: When Operation is set to PCIe-GenI (2.5 GHz), a 2500-MHz signal is output, synchronized with the selected 10-MHz reference signal.<br>When Operation is set to PCIe-GenII (5 GHz), a 5000-MHz signal is output, synchronized with the selected 10-MHz reference signal. |
| Jitter | Jitter | This item is enabled only when SSC is set to OFF.<br>ON: A clock signal is output with jitter added.<br>OFF: An unmodulated clock signal is output. |
| | Modulation Source | Internal: Select when adding jitter with the internal modulation signal source. When Internal is selected, the Frequency and Amplitude spin boxes are displayed as shown in Fig. 4.2.2-2, to set the frequency and amplitude depth of the sine wave jitter.<br>External: Select when adding jitter with an external signal source. When External is selected, the Frequency Range and Amplitude Range spin boxes are displayed as shown in Fig. 4.2.2-3. Select the ranges of the frequency and amplitude depth to be modulated by referring to Figs 4.2.2-5 through 4.2.2-9, and set in these spin boxes. Note that Amplitude is fixed to 0.22 UI when Frequency Range is set to Full.<br>External I/Q: Select when using the I and Q signals for modulation. The Jitter field becomes as shown in Fig. 4.2.2-4, which does not provide any items for setting the frequency and amplitude depth. |

**Table 4.2.2-1 Items in operation window when MU181000A/B-x01 is installed (Cont'd)**

| Item | | Functions |
|---|---|---|
| Jitter | Trigger Source | When Fc is set to greater than 800 MHz, a signal obtained by dividing the clock output frequency (Fc) by 1 or 64 is output from the Trigger Out connector.<br>F/1: A signal with Fc divided by 1 is output. This can be selected only when Fc is set to greater than 6400 MHz.<br>OFF: A signal with Fc divided by 64 is output. |

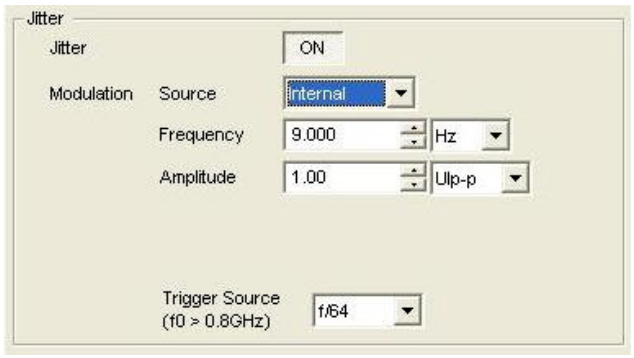



**Fig. 4.2.2-2 Jitter setting field when Internal is selected**

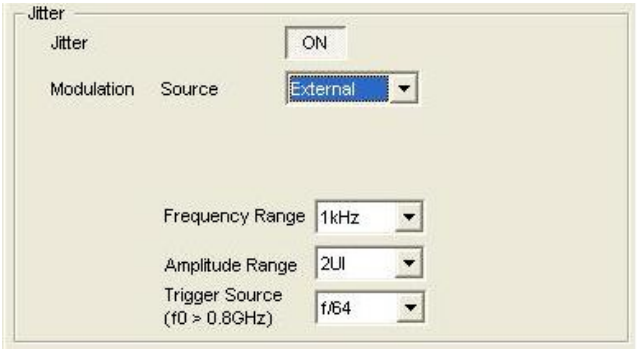



**Fig. 4.2.2-3 Jitter setting field when External is selected**

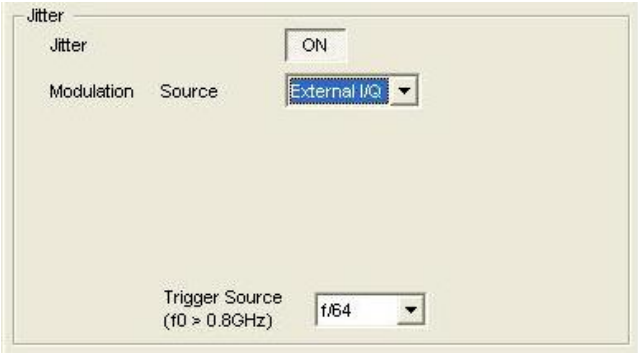



**Fig. 4.2.2-4 Jitter setting field when External I/Q is selected**

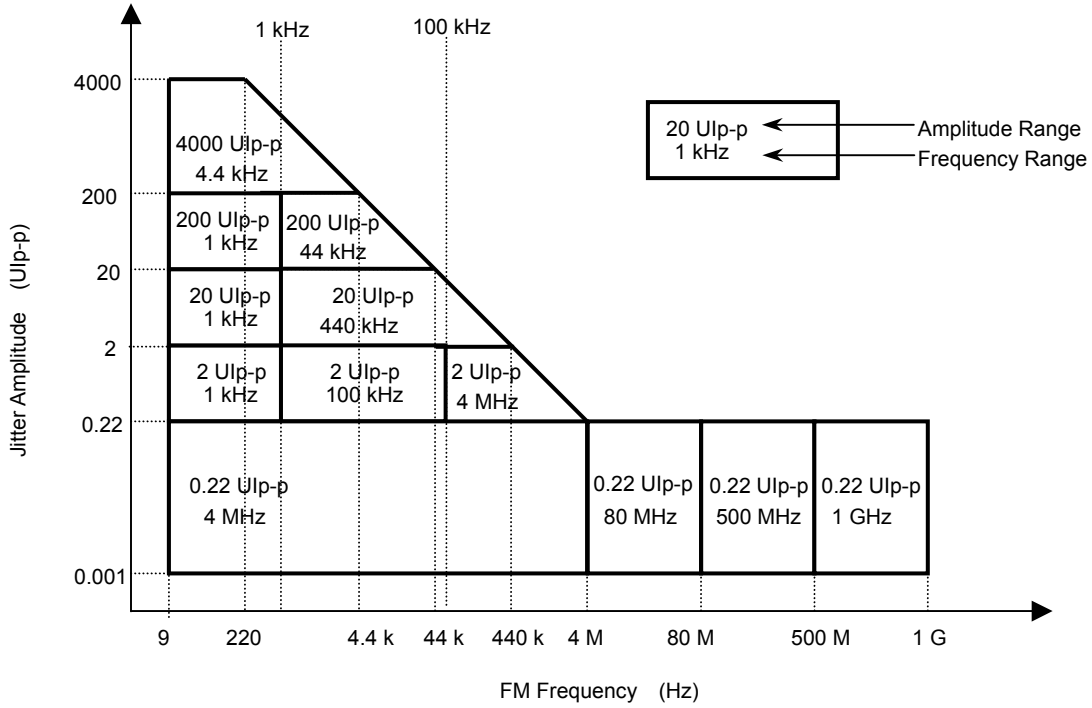



**Fig. 4.2.2-5 Range selection when 6.4 GHz < Fc ≤ 12.5 GHz**

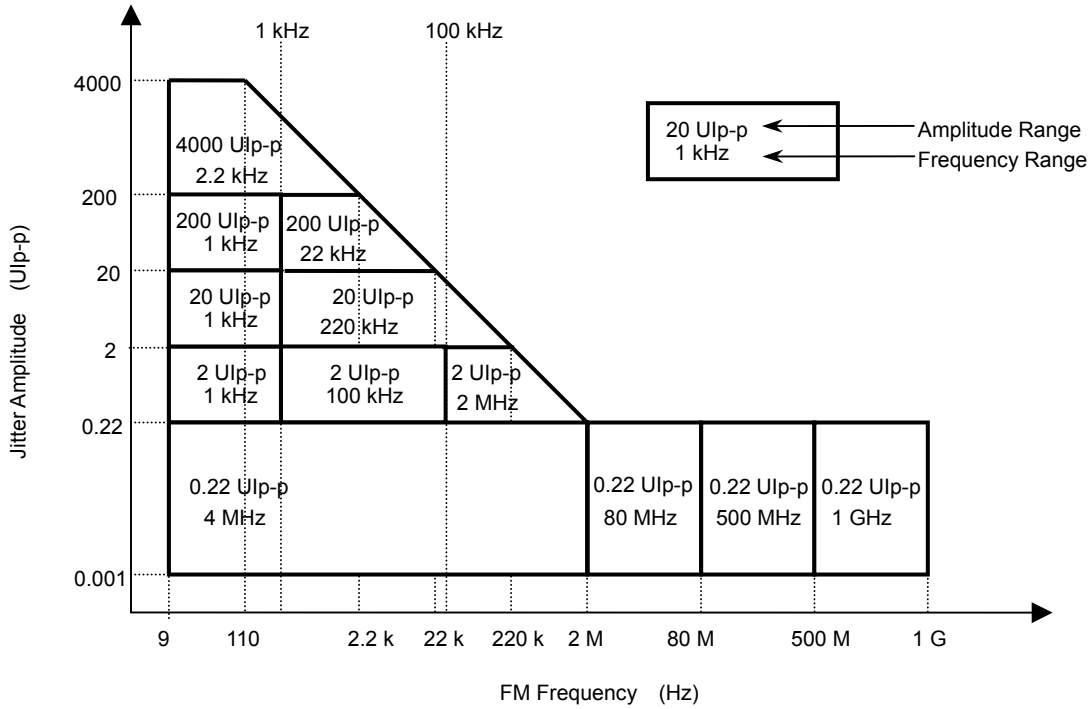



**Fig. 4.2.2-6 Range selection when 3.2 GHz < Fc ≤ 6.4 GHz**

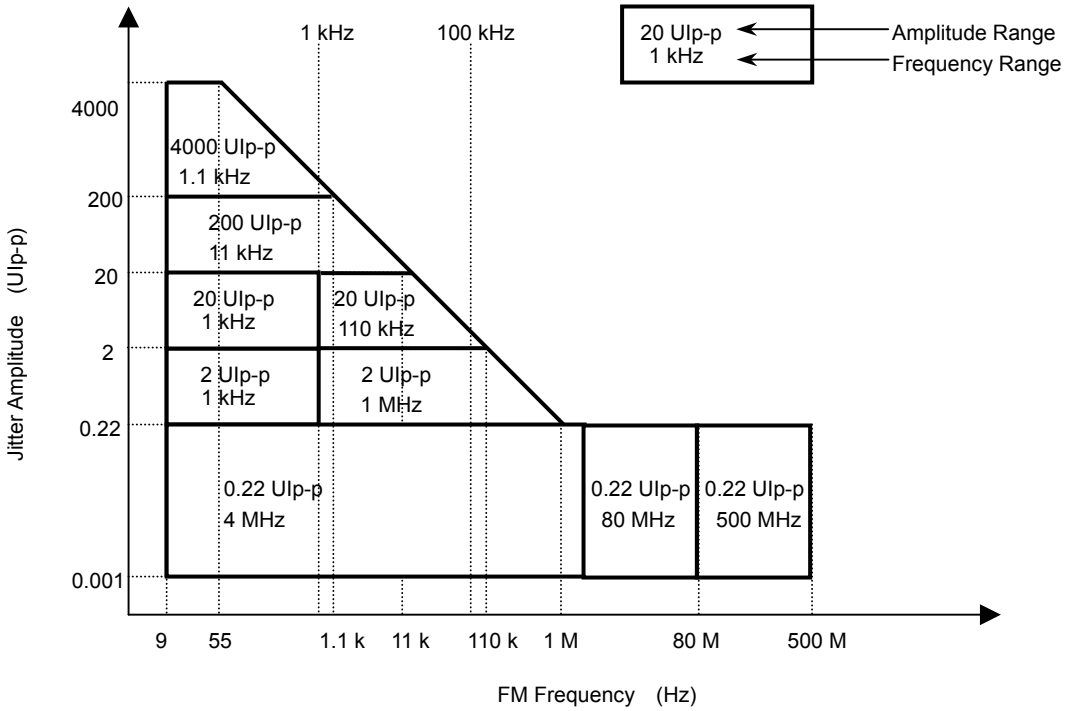



**Fig. 4.2.2-7 Range selection when 1.6 GHz < Fc ≤ 3.2 GHz**

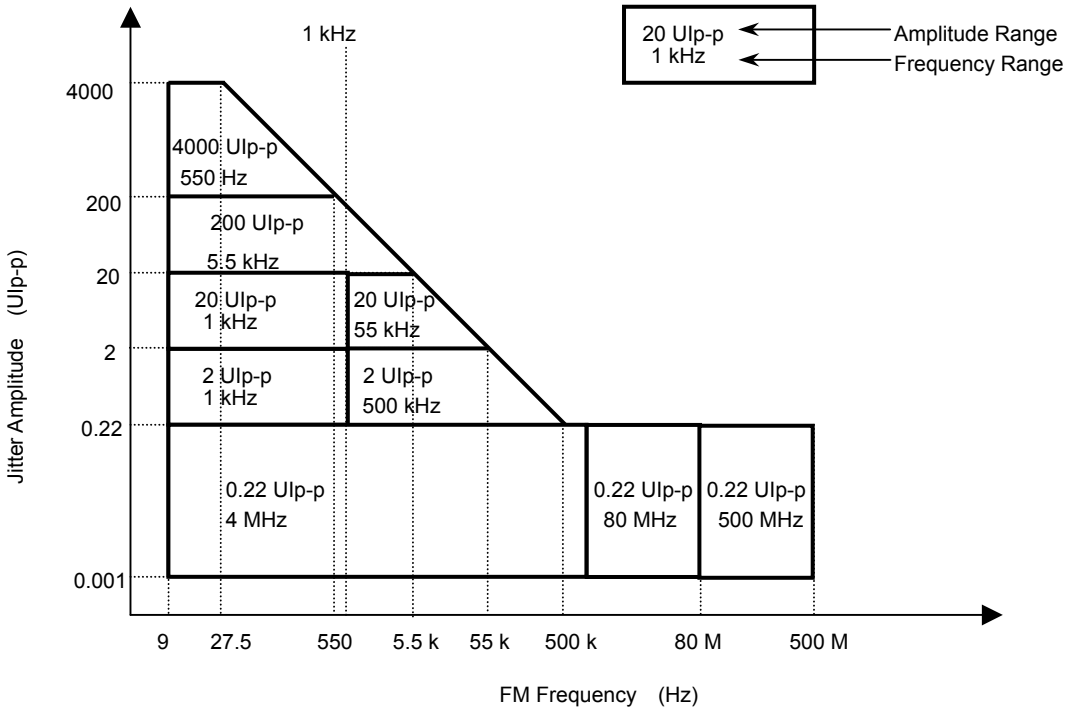



**Fig. 4.2.2-8 Range selection when 0.8 GHz < Fc ≤ 1.6 GHz**

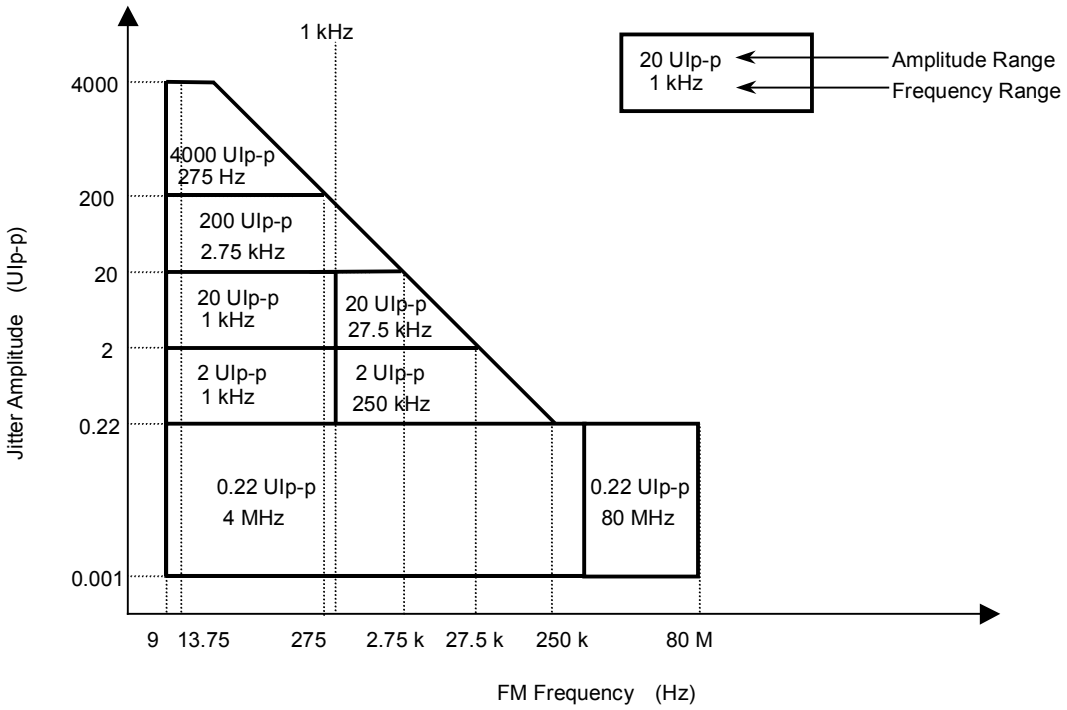



**Fig. 4.2.2-9 Range selection when 0.1 GHz ≤ Fc ≤ 0.8 GHz**

***Note:***

For details of the operation method and cautions when inputting a jitter-modulated signal to the MU181020A 12.5 Gbit/s PPG (with Option x30 Variable Data Delay installed) or the MU181040A 12.5 Gbit/s ED (with Option x30 Variable Clock Delay), see section 5.1.2 "Setting delay" of the MU181020A 12.5 Gbit/s PPG Operation Manual or 5.1.5 "Setting items when input is selected" of the MU181040A 12.5 Gbit/s ED Operation Manual.

# Section 5 Use Example

This section provides an MU181000A/B usage example.

# 5.1 MU181000A/B Usage Example

## 5.1.1 Example when using the MU181000A/B as a clock signal source

The following shows an example of how to use the MU181000A/B as the clock signal source for the MU181020A. In this section, drawings of the MU181000A are used for explanation, while the contents are common to the MU181000B.

1. Connect the Clock Output connector of the MU181000A/B and the Ext. Clock Input connector of the MU181020A, using a coaxial cable.

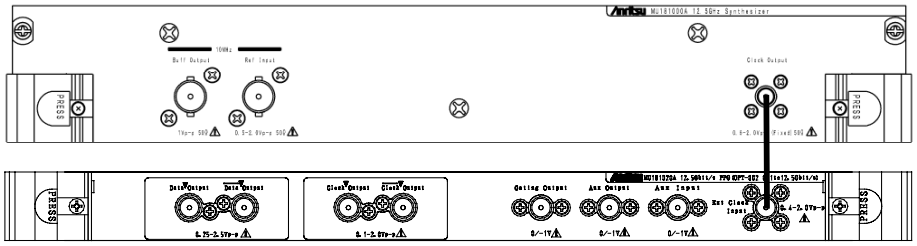



**Fig. 5.1.1-1 Connection with MU181020A**

2. Generate Data and Clock signals by referring to the MU181020A 12.5 Gbit/s PPG Operation Manual.

## 5.1.2 Example when using an external modulation signal source

1. When inputting a modulation signal to the Jitter Ext Input connector

   The jitter amplitude is determined depending on the Frequency Range and Amplitude Range settings in the operation window and the amplitude of the input signal. Adjust the amplitude of the signal source when more accurate jitter amplitude than the guaranteed accuracy level is required. The jitter amplitude can be obtained by measuring the carrier and sideband power using the spectrum analyzer. In this event, however, if the jitter amplitude is more than 0.4 UIp-p, be sure to divide the frequency so that the jitter amplitude is lowered to 0.4 UIp-p or less before performing measurement with the spectrum analyzer.

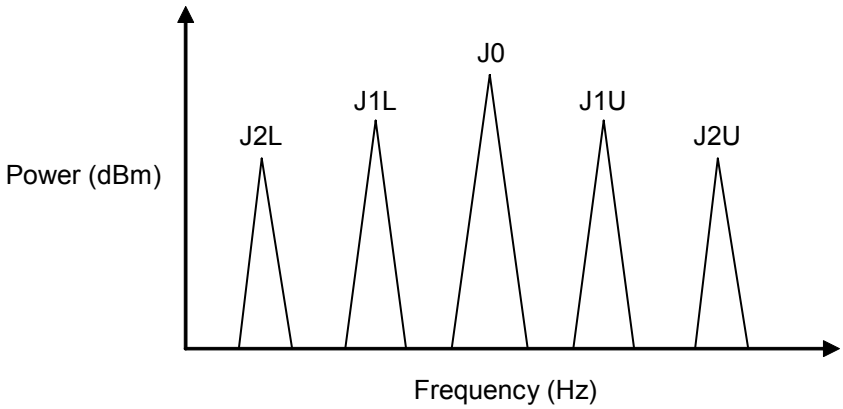



**Fig. 5.1.2-1 Spectrum waveform**

J0: Carrier power (dBm)

J1U: Power of the upper frequency of the first sideband (dBm)

J1L: Power of the lower frequency of the first sideband (dBm)

J2U: Power of the upper frequency of the second sideband (dBm)

J2L: Power of the lower frequency of the second sideband (dBm)

Jitter amplitude calculation method

J1 = (J1U + J1L)/2

J2 = (J2U + J2L)/2

j0 = 10^ (J0/20)

j1 = 10^ (J1/20)

2 = 10^ (J2/20)

Jitter amplitude = (2 × j1/(j0 + j2))/π × Division ratio (UIp-p)

When the amplitude is set to 0.22 UI, the waveform may be distorted for an amplitude of more than 0.1 UIp-p. In order to adjust the peak-to-peak value of the phase deviation more precisely, correct the calculated jitter amplitude (obtained from the expression above) as follows:

UIm: Jitter amplitude (UIp-p) calculated from the spectrum power described above

Jitter amplitude = UIm × (1 – (0.03 × UIm – 0.0024) × 10 (UIp-p))

2. When executing modulation by inputting a signal to the Ext IQ connector

When adding a sine wave jitter, generate the I and Q signals as shown below, using an arbitrary waveform generator, and input them.

Calculate the actual modulation amount from the spectrum power, and check the difference from the set modulation amount before using this unit.

I signal = 0.5 × sin (UIs × π × sin (2 × π × t/T)) (V)

Q signal = 0.5 × cos (UIs × π × sin (2 × π × t/T)) (V)

UIs: Set jitter amplitude (UIp-p)

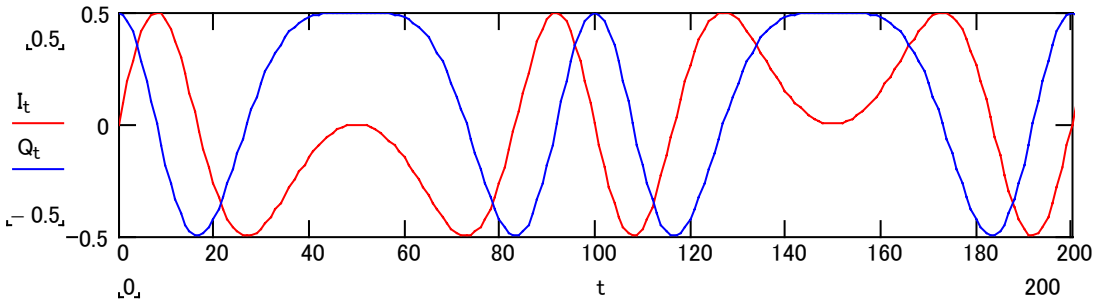



**Fig. 5.1.2-2 Example of waveforms of I and Q signals when jitter amplitude is 2 UIp-p**

# Section 6 Performance Test

This section describes the performance testing of the MU181000A/B.

# 6.1 Overview

Performance tests are executed to check that the major functions of the MU181000A/B meet the required specifications. Execute performance tests at acceptance inspection, operation check after repair, and periodic (once every six months) testing.

# 6.2 Devices Required for Performance Tests

Before starting performance tests, warm up the MU181000A/B and the measuring instruments for at least 30 minutes. Table 6.2-1 shows the devices required for performance tests.

**Table 6.2-1 Devices required for MU181000A/B performance test**

| Device | Required Performance |
|---|---|
| Sampling oscilloscope | 50 GHz or more band |
| Spectrum analyzer | 50 GHz or more band |
| Divider | 10,000 or more divisions |
| Signal generator | 2 GHz or more |
| Function generator | 9 Hz or less |

***Note:***

Before starting the performance tests, warm up the device under test and the measuring instruments for at least 30 minutes and wait until they become sufficiently stabilized, unless otherwise specified. Additional conditions are required for maximum measurement accuracy: measurements must be performed at room temperature, fluctuations of AC power supply voltage must be small, and noise, vibration, dust, and humidity must be insignificant.

# 6.3 Performance Test Items

This section describes the following test items. In this section, drawings of the MU181000A are used for explanation, while the contents are common to the MU181000B.

(1) Waveform
(2) Phase noise
(3) Internal sine wave jitter
(4) External sine wave jitter
(5) Triangular wave modulation
(6) Trigger output waveform

***Note:***

MU181000A/B-x01 must be installed when executing test items (3) to (6).

## 6.3.1 Measuring waveform

Measure the waveform at the Clock Output connector. Connection with the measuring instruments is shown in Fig. 6.3.1-1.

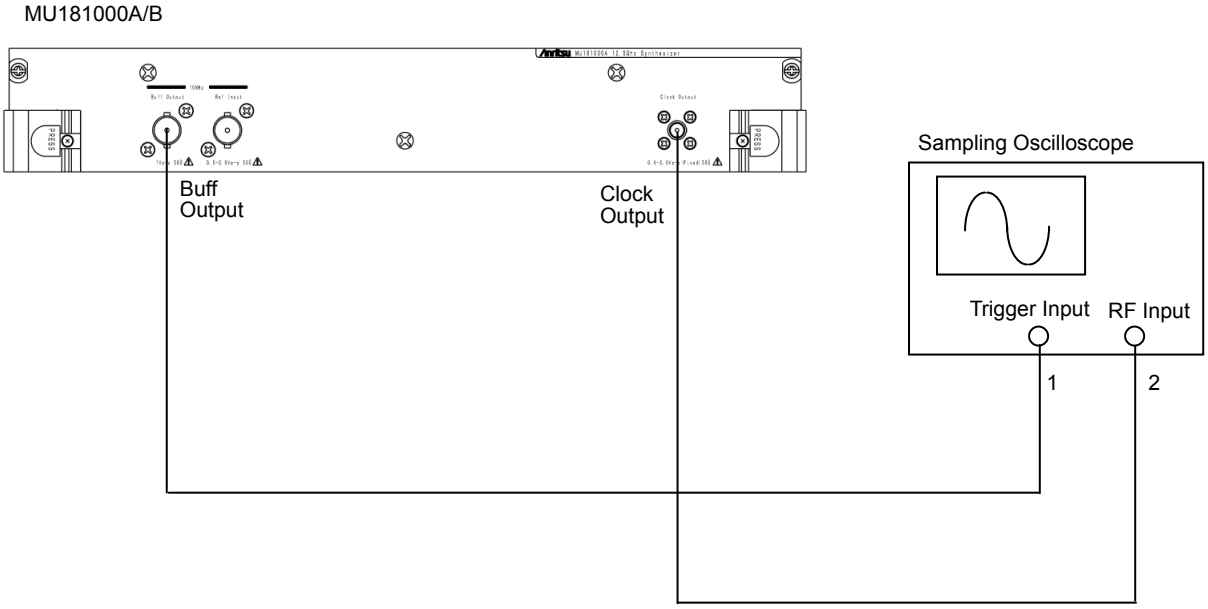



**Fig. 6.3.1-1 Connection diagram for waveform measurement**

1. Connect the signal output from the Buff Output connector to the Trigger Input connector of the sampling oscilloscope.
2. Connect the clock signal output from the Clock Output connector to the RF Input connector of the sampling oscilloscope. Set the averaging to 64 (times) for the sampling scope.

## ⚠ CAUTION

**Use an appropriate attenuator for each input connector when connecting to the input connector of the sampling oscilloscope. Improper connection may damage the sampling oscilloscope.**

3. Set as follows in the MU181000A/B operation window:
   Center Frequency: 12,500 MHz
   Offset: 0 ppm
   Reference Clock Source: Internal
4. Use the averaging function of the sampling oscilloscope to measure the waveform, and then check that the level of the measured waveform falls within the range shown in Fig. 6.3.1-2.

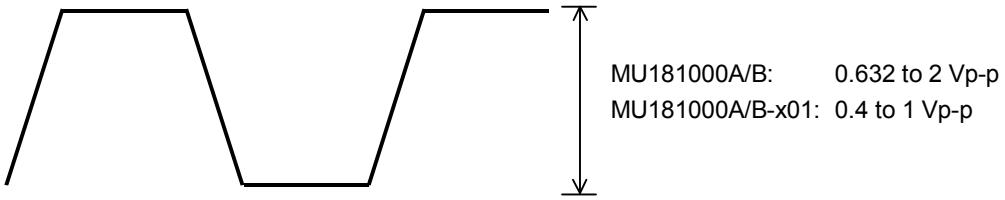



**Fig. 6.3.1-2 Example of waveform measured by sampling oscilloscope**

5. Change the Center Frequency value in the MU181000A/B operation window as shown below, and perform measurement in the same way. Check that the waveform level falls within the range shown in Fig. 6.3.1-2.
   Center Frequency: 10,000 MHz, 5,000 MHz, 1,000 MHz, 100 MHz

## 6.3.2 Measuring phase noise

Measure the phase noise at the Clock Output connector. Connection with the measuring instruments is shown in Fig. 6.3.2-1.

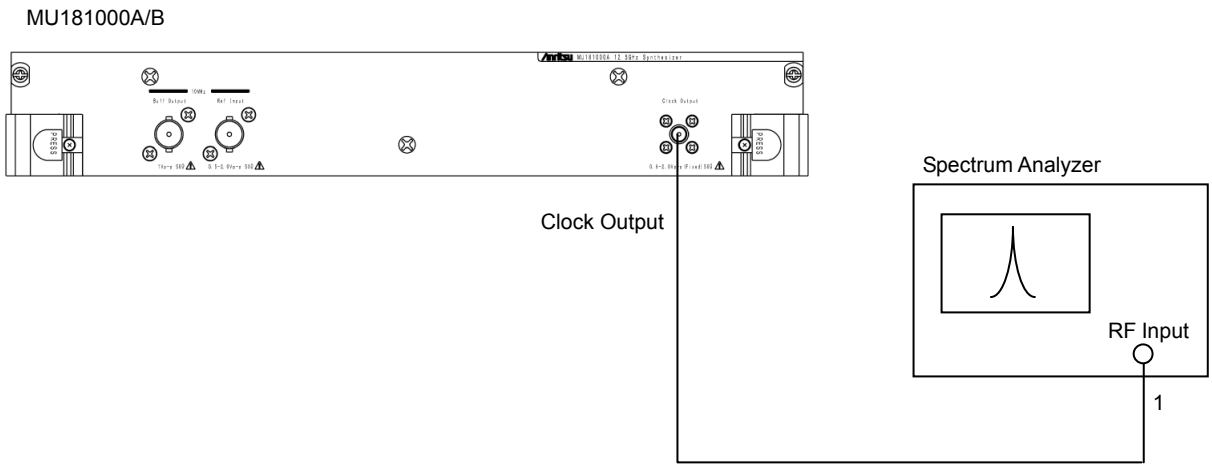



**Fig. 6.3.2-1 Connection diagram for phase noise measurement**

1. Connect the clock signal output from the Clock Output connector to the RF Input connector of the spectrum analyzer.
2. Set as follows in the MU181000A/B operation window:
   Center Frequency: 12,500 MHz
   Offset: 0 ppm
   Reference Clock Source: Internal
3. Check that the phase noise measured by the spectrum analyzer falls within one of the ranges shown below:
   ≤–61 dBc/Hz at 1 kHz offset
   ≤–81 dBc/Hz at 10 kHz offset
   ≤–90 dBc/Hz at 100 kHz offset
4. Change the Center Frequency value in the MU181000A/B operation window as shown below, and perform measurement in the same way. Check that the phase noise falls within the range shown in Step 3 above.
   Center Frequency: 10,000 MHz, 5,000 MHz, 1,000 MHz, 100 MHz

**⚠ CAUTION**

**Use an appropriate attenuator for each input connector when connecting to the input connector of the spectrum analyzer. Improper connection may damage the spectrum analyzer.**

## 6.3.3 Measuring internal sine wave jitter

Measure the jitter amplitude at the Clock Output connector. Connection with the measuring instrument is shown in Figs. 6.3.3-1 and 6.3.3-2.

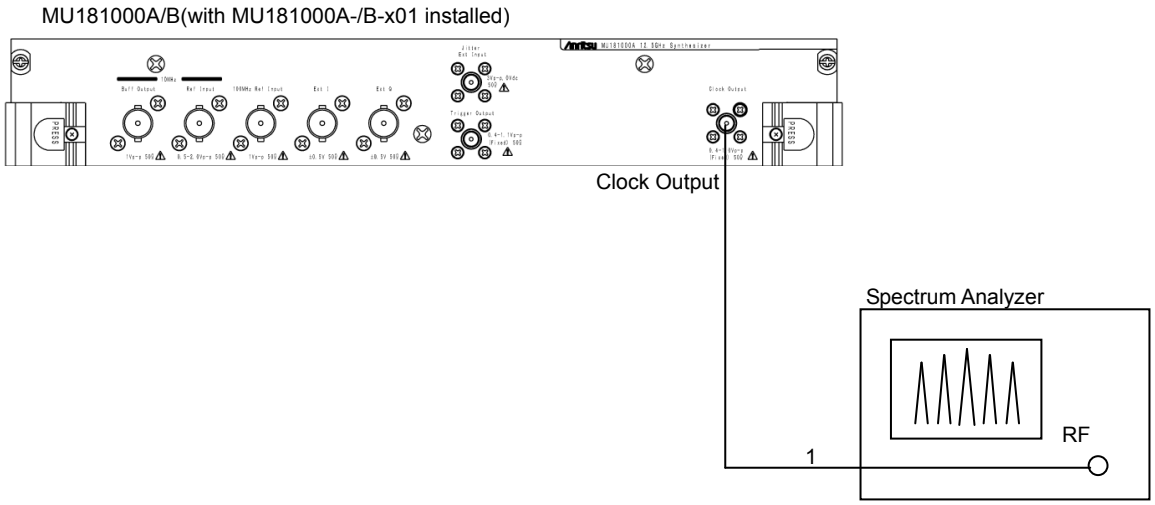



**Fig. 6.3.3-1 Connection diagram for internal sine wave jitter measurement of 0.4 UIp-p or less**

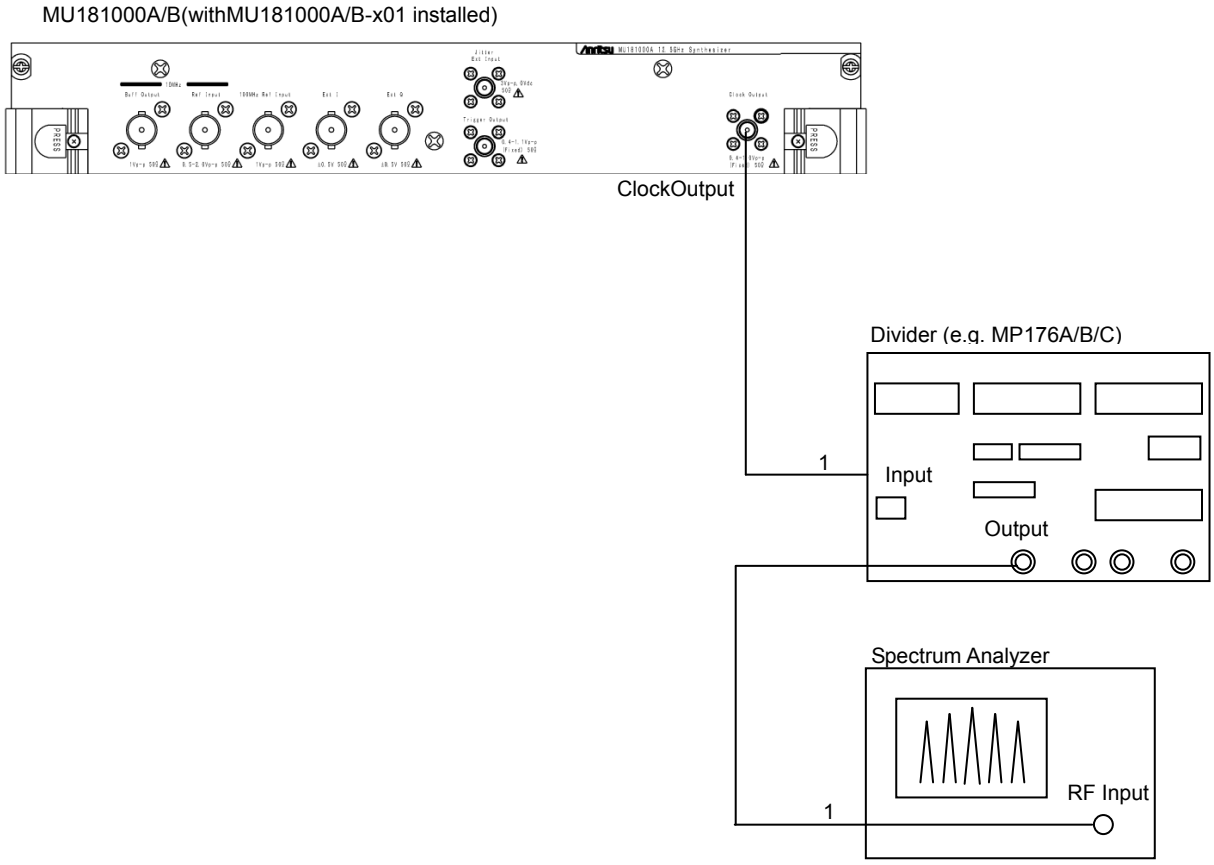



**Fig. 6.3.3-2 Connection diagram for internal sine wave jitter measurement of more than 0.4 UIp-p**

1. When the jitter amplitude is set to 0.4 UIp-p or less, connect the clock signal output from the Clock Output connector to the RF Input connector of the spectrum analyzer.
   When the jitter amplitude is set to more than 0.4 UIp-p, connect the clock signal output from the Clock Output connector to the Input connector of the divider, and connect the Output connector of the divider and the RF Input connector of the spectrum analyzer.
2. Set as follows in the MU181000A/B operation window:
   Operation: Variable
   Center Frequency: See Table 6.3.3-1.
   Offset: 0 ppm
   Reference Clock Source: Internal
   Jitter: ON
   Modulation Source: Internal
   Frequency: See Table 6.3.3-1.
   Amplitude: See Table 6.3.3-1.

**Table 6.3.3-1 Setting points for internal jitter measurement**

| Center Frequency | Frequency | Jitter Amplitude |
|---|---|---|
| 12500 MHz | 9 Hz | 4000 UIp-p |
| | 220 Hz | 4000 UIp-p |
| | 1 kHz | 880 UIp-p |
| | 10 kHz | 88 UIp-p |
| | 100 kHz | 8.8 UIp-p |
| | 1 MHz | 0.88 UIp-p |
| | 4 MHz | 0.22 UIp-p |
| | 10 MHz | 0.22 UIp-p |
| | 20 MHz | 0.22 UIp-p |
| | 50 MHz | 0.22 UIp-p |
| | 80 MHz | 0.22 UIp-p |
| 9953.28 MHz | 10 Hz | 22 UIp-p |
| | 40 kHz | 22 UIp-p |
| | 4 MHz | 0.22 UIp-p |
| | 10 MHz | 0.22 UIp-p |
| | 80 MHz | 0.22 UIp-p |

**Table 6.3.3-1 Setting points for internal jitter measurement (Cont'd)**

| Center Frequency | Frequency | Jitter Amplitude |
|---|---|---|
| 6400 MHz | 9 Hz | 4000 UIp-p |
| | 110 Hz | 4000 UIp-p |
| | 1 kHz | 440 UIp-p |
| | 10 kHz | 44 UIp-p |
| | 100 kHz | 4.4 UIp-p |
| | 1 MHz | 0.44 UIp-p |
| | 2 MHz | 0.22 UIp-p |
| | 5 MHz | 0.22 UIp-p |
| | 10 MHz | 0.22 UIp-p |
| | 20 MHz | 0.22 UIp-p |
| | 40 MHz | 0.22 UIp-p |
| 3200 MHz | 9 Hz | 4000 UIp-p |
| | 55 Hz | 4000 UIp-p |
| | 100 Hz | 2200 UIp-p |
| | 1 kHz | 220 UIp-p |
| | 10 kHz | 22 UIp-p |
| | 100 kHz | 2.2 UIp-p |
| | 1 MHz | 0.22 UIp-p |
| | 5 MHz | 0.22 UIp-p |
| | 10 MHz | 0.22 UIp-p |
| 2488.32 MHz | 10 Hz | 22 UIp-p |
| | 10 kHz | 22 UIp-p |
| | 1 MHz | 0.22 UIp-p |
| | 10 MHz | 0.22 UIp-p |
| | 20 MHz | 0.22 UIp-p |
| 1600 MHz | 9 Hz | 4000 UIp-p |
| | 27.5 Hz | 4000 UIp-p |
| | 100 Hz | 1100 UIp-p |
| | 1 kHz | 110 UIp-p |
| | 10 kHz | 11 UIp-p |
| | 100 kHz | 1.1 UIp-p |
| | 500 kHz | 0.22 UIp-p |
| | 1 MHz | 0.22 UIp-p |
| | 2 MHz | 0.22 UIp-p |
| | 5 MHz | 0.22 UIp-p |
| | 10 MHz | 0.22 UIp-p |

**Table 6.3.3-1 Setting points for internal jitter measurement (Cont'd)**

| Center Frequency | Frequency | Jitter Amplitude |
|---|---|---|
| 1244.16 MHz | 10 Hz | 22 UIp-p |
| | 5 kHz | 22 UIp-p |
| | 500 kHz | 0.22 UIp-p |
| | 10 MHz | 0.22 UIp-p |
| 800 MHz | 9 Hz | 4000 UIp-p |
| | 13.75 Hz | 4000 UIp-p |
| | 100 Hz | 550 UIp-p |
| | 1 kHz | 55 UIp-p |
| | 10 kHz | 5.5 UIp-p |
| | 100 kHz | 0.55 UIp-p |
| | 250 kHz | 0.22 UIp-p |
| | 1 MHz | 0.22 UIp-p |
| | 2 MHz | 0.22 UIp-p |
| | 5 MHz | 0.22 UIp-p |
| 622.08 MHz | 10 Hz | 22 UIp-p |
| | 2.5 kHz | 22 UIp-p |
| | 250 kHz | 0.22 UIp-p |
| | 5 MHz | 0.22 UIp-p |
| 155.52 MHz | 10 Hz | 22 UIp-p |
| | 2.5 kHz | 22 UIp-p |
| | 250 kHz | 0.22 UIp-p |
| | 5 MHz | 0.22 UIp-p |
| 100 MHz | 10 Hz | 22 UIp-p |
| | 200 Hz | 22 UIp-p |
| | 20 kHz | 0.22 UIp-p |
| | 1 MHz | 0.22 UIp-p |
| | 5 MHz | 0.22 UIp-p |

3. Measure the carrier and sideband power using the spectrum analyzer, and calculate the jitter amplitude by referring to the description in Section 5.1.2 "Example when using an external modulation signal source" Next, check that the calculation result falls within one of the ranges shown below.

   When Center Frequency is less than 1 GHz:

   ±0.01 UI ± Q% when the set amplitude is 0.001 to 2.19 UIp-p
   ±0.2 UI ± Q% when the set amplitude is 2.2 to 21.99 UIp-p
   ±2 UI ± Q% when the set amplitude is 22 to 4000 UIp-p

   When Center Frequency is 1 GHz or higher:

   ±0.02 UI ± Q% when the set amplitude is 0.001 to 2.19 UIp-p
   ±0.2 UI ± Q% when the set amplitude is 2.2 to 21.99 UIp-p
   ±2 UI ± Q% when the set amplitude is 22 to 4000 UIp-p

   Note that the value of Q differs depending on the modulation frequency, as follows:

   9 Hz ≤ Frequency ≤ 500 kHz: 7
   500 kHz < Frequency ≤ 2 MHz: 12
   2 MHz < Frequency ≤ 80 MHz: 15

**⚠ CAUTION**

**Use an appropriate attenuator for each input connector when connecting to the input connector of the spectrum analyzer. Improper connection may damage the spectrum analyzer.**

## 6.3.4 Measuring external sine wave jitter

Measure the jitter amplitude at the Clock Output connector. Connection with the measuring instruments is shown in Figs. 6.3.4-1 and 6.3.4-2.

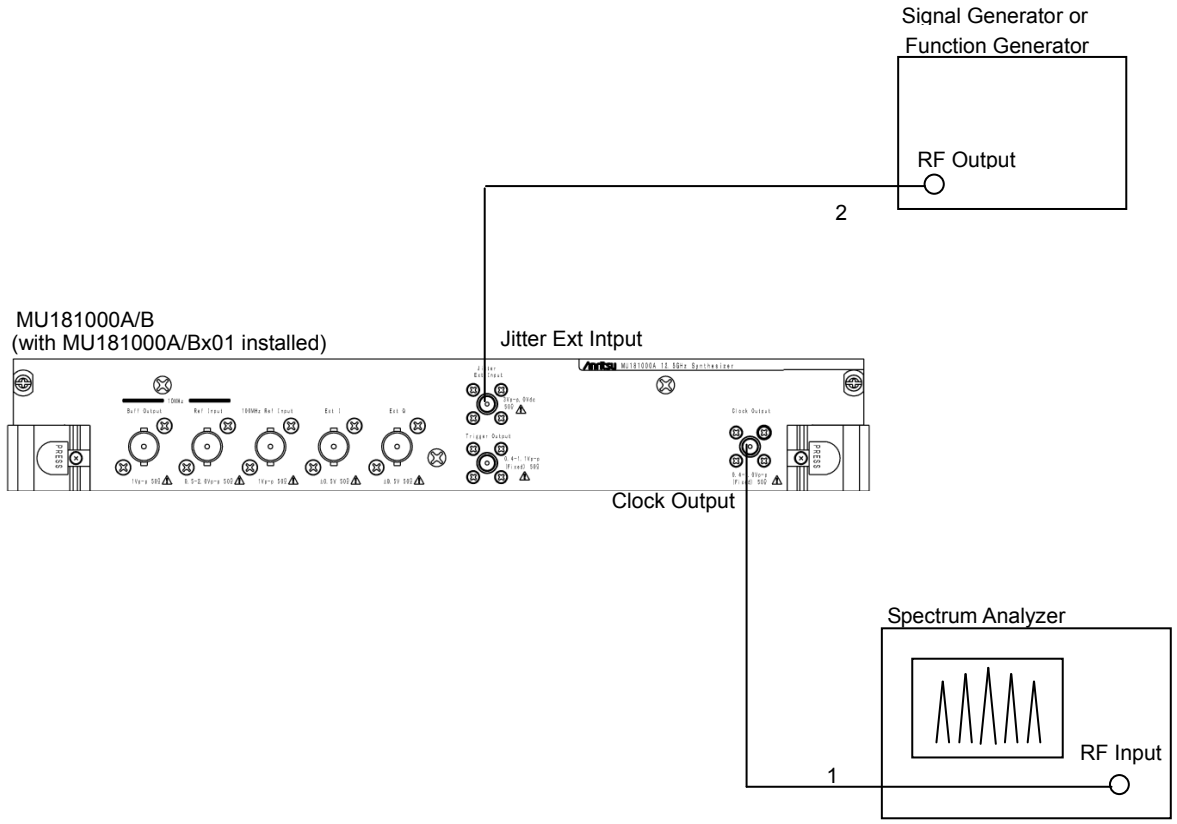



**Fig. 6.3.4-1 Connection diagram for external sine wave jitter measurement of 0.4 UIp-p or less**

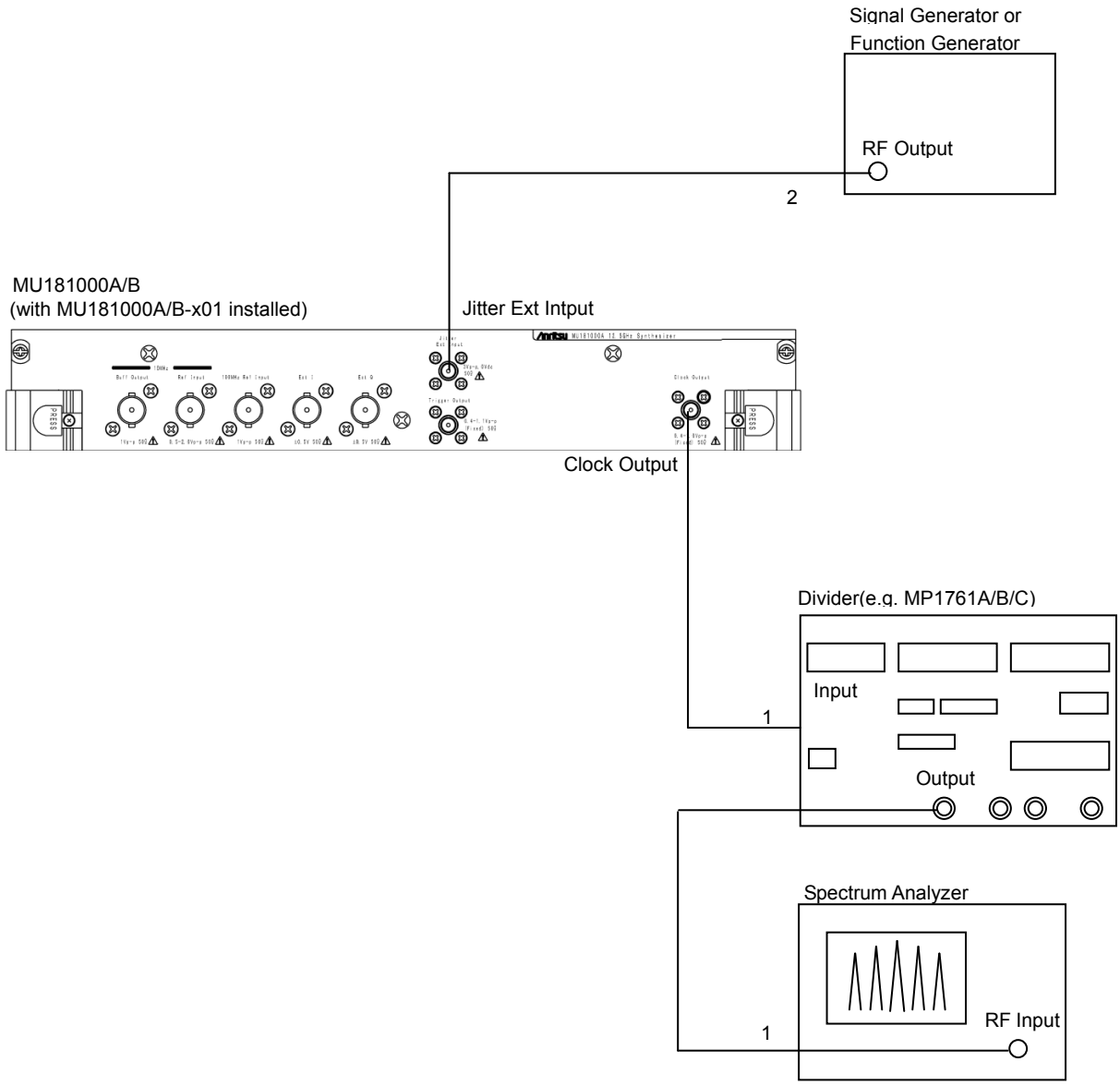



**Fig. 6.3.4-2 Connection diagram for external sine wave jitter measurement of more than 0.4 UIp-p**

1. When the jitter amplitude is set to 0.4 UIp-p or less, connect the clock signal output from the Clock Output connector to the RF Input connector of the spectrum analyzer.
   When the jitter amplitude is set to more than 0.4 UIp-p, connect the clock signal output from the Clock Output connector to the Input connector of the divider, and connect the Output connector of the divider and the RF Input connector of the spectrum analyzer.
2. Connect the sine wave output of the external modulation signal source, signal generator or function generator, to the Jitter Ext Input connector of the MU181000A/B. When a 50-Ω load is attached to the edge of the cable on the Jitter Ext Input side, perform calibration before input so that 0.5 Vp-p is obtained. See Table 6.3.4-1 for the output frequency of the modulation signal source.
3. Set as follows in the MU181000A/B operation window:
   Operation: Variable
   Center Frequency: See Table 6.3.4-1.
   Offset: 0 ppm
   Reference Clock Source: Internal
   Jitter: ON
   Modulation Source: External
   Frequency: See Table 6.3.4-1.
   Amplitude: See Table 6.3.4-1.
4. Measure the carrier and sideband power using the spectrum analyzer, and calculate the jitter amplitude by referring to the description in Section 5.1.2 “Example when using an external modulation signal source.” Then check that the calculation result falls within one of the ranges shown in Table 6.3.4-1.

**Table 6.3.4-1 Setting points for external jitter measurement**

| Center Frequency | Frequency Range | Amplitude Range | Input Frequency | Amplitude |
|---|---|---|---|---|
| 12500 MHz | 4.4 kHz | 4000 UI | 220 Hz | 1000 UIp-p ± 300 UI |
| | 44 kHz | 200 UI | 4.4 kHz | 100 UIp-p ± 30 UI |
| | 440 kHz | 20 UI | 44 kHz | 10 UIp-p ± 3 UI |
| | 4 MHz | 2 UI | 440 kHz | 1 UIp-p ± 0.3 UI |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.03 UI |
| | 80 MHz | 0.22 UI | 80 MHz | 0.1 UIp-p ± 0.03 UI |
| | 500 MHz | 0.22 UI | 500 MHz | 0.1 UIp-p ± 0.03 UI |
| | 1 GHz | 0.22 UI | 1 GHz | 0.1 UIp-p ± 0.03 UI |
| 6400 MHz | 2.2 kHz | 4000 UI | 110 Hz | 1000 UIp-p ± 300 UI |
| | 22 kHz | 200 UI | 2.2 kHz | 100 UIp-p ± 30 UI |
| | 220 kHz | 20 UI | 22 kHz | 10 UIp-p ± 3 UI |
| | 2 MHz | 2 UI | 220 kHz | 1 UIp-p ± 0.3 UI |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.03 UI |
| | 80 MHz | 0.22 UI | 80 MHz | 0.1 UIp-p ± 0.03 UI |
| | 500 MHz | 0.22 UI | 500 MHz | 0.1 UIp-p ± 0.03 UI |
| | 1 GHz | 0.22 UI | 1 GHz | 0.1 UIp-p ± 0.03 UI |
| 3200 MHz | 1.1 kHz | 4000 UI | 55 Hz | 1000 UIp-p ± 300 UI |
| | 11 kHz | 200 UI | 1.1 kHz | 100 UIp-p ± 30 UI |
| | 110 kHz | 20 UI | 11 kHz | 10 UIp-p ± 3 UI |
| | 1 MHz | 2 UI | 110 kHz | 1 UIp-p ± 0.3 UI |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.03 UI |
| | 80 MHz | 0.22 UI | 80 MHz | 0.1 UIp-p ± 0.03 UI |
| | 500 MHz | 0.22 UI | 500 MHz | 0.1 UIp-p ± 0.03 UI |

**Table 6.3.4-1 Setting points for external jitter measurement (Cont'd)**

| Center Frequency | Frequency Range | Amplitude Range | Input Frequency | Amplitude |
|---|---|---|---|---|
| 1600 MHz | 550 Hz | 4000 UI | 27.5 Hz | 1000 UIp-p ± 300 UI |
| | 5.5 kHz | 200 UI | 550 Hz | 100 UIp-p ± 30 UI |
| | 55 kHz | 20 UI | 5.5 kHz | 10 UIp-p ± 3 UI |
| | 500 kHz | 2 UI | 55 kHz | 1 UIp-p ± 0.3 UI |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.03 UI |
| | 80 MHz | 0.22 UI | 80 MHz | 0.1 UIp-p ± 0.03 UI |
| | 500 MHz | 0.22 UI | 100 MHz | 0.1 UIp-p ± 0.03 UI |
| 800 MHz | 275 Hz | 4000 UI | 13.75 Hz | 1000 UIp-p ± 300 UI |
| | 2.75 kHz | 200 UI | 275 Hz | 100 UIp-p ± 30 UI |
| | 27.5 kHz | 20 UI | 2.75 kHz | 10 UIp-p ± 3 UI |
| | 250 kHz | 2 UI | 27.5 kHz | 1 UIp-p ± 0.3 UI |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.03 UI |
| | 80 MHz | 0.22 UI | 20 MHz | 0.1 UIp-p ± 0.03 UI |
| 100 MHz | 275 Hz | 4000 UI | 13.75 Hz | 1000 UIp-p ± 300 UI |
| | 2.75 kHz | 200 UI | 275 Hz | 100 UIp-p ± 30 UI |
| | 27.5 kHz | 20 UI | 2.75 kHz | 10 UIp-p ± 3 UI |
| | 250 kHz | 2 UI | 27.5 kHz | 1 UIp-p ± 0.3 UI |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.03 UI |
| | 80 MHz | 0.22 UI | 5 MHz | 0.1 UIp-p ± 0.03 UI |

## ⚠ CAUTION

**Use an appropriate attenuator for each input connector when connecting to the input connector of the spectrum analyzer. Improper connection may damage the spectrum analyzer.**

## 6.3.5 Measuring triangular wave modulation

Measure the phase deviation at the Clock Output connector. Connection with the measuring instruments is shown in Fig. 6.3.5-1.

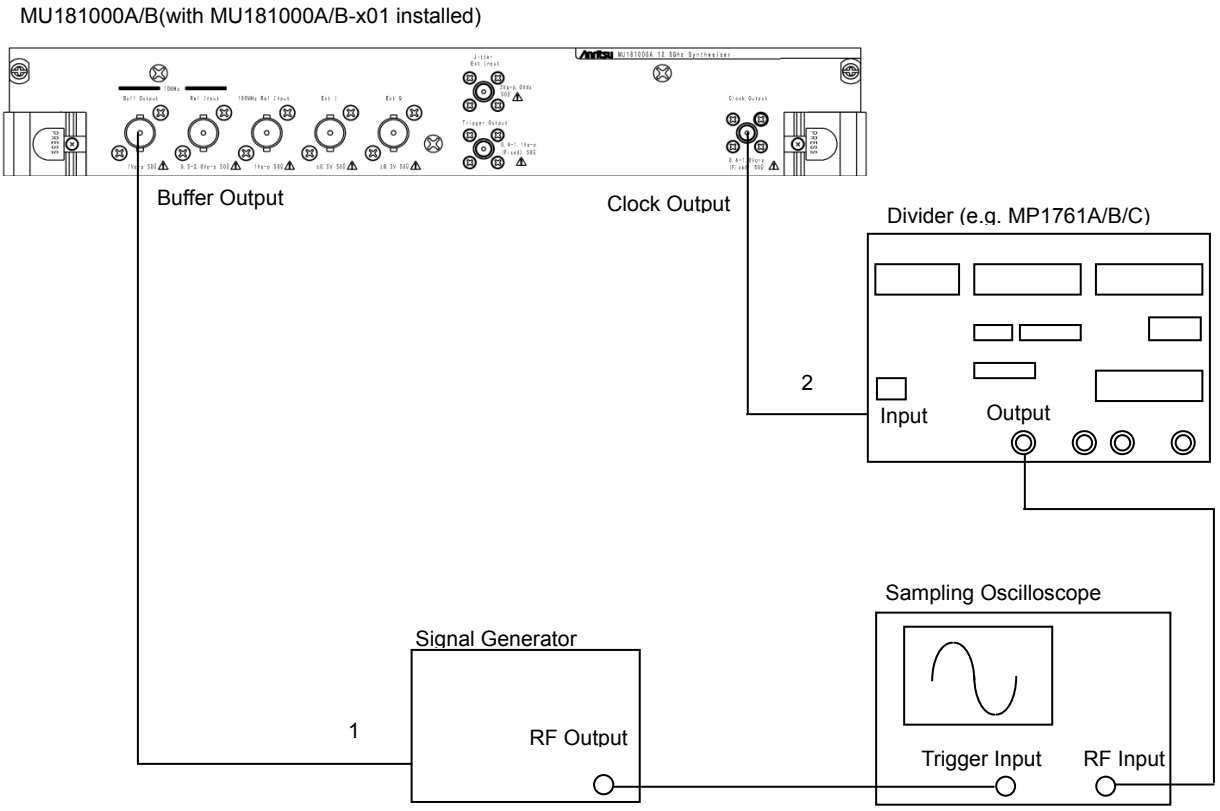



**Fig. 6.3.5-1 Connection diagram for triangular wave modulation measurement**

1. Connect the Buff Output signal to the RF Input connector of the signal generator.
   Set the output frequency of the signal generator to 9.765625 MHz, and the output level to within the operation guaranteed range of the sampling oscilloscope. Next, connect the RF Output connector of the signal generator and the Trigger Input connector of the sampling oscilloscope.
2. Connect the Clock Output signal to the Input connector of the divider. Next, connect the Output of the divider and the RF Input connector of the sampling oscilloscope.
   See Table 6.3.5-1 for the division ratio of the divider.

**⚠ CAUTION**

**Use an appropriate attenuator for each input connector when connecting to the input connector of the spectrum analyzer. Improper connection may damage the spectrum analyzer.**

3. Set as follows in the MU181000A/B operation window:
   Operation: See Table 6.3.5-1.
   Offset: 0 ppm
   SSC: ON (Inf Ref)
   Spread Method: Center

**Table 6.3.5-1 Setting for triangular wave modulation measurement**

| Operation | Division Ratio for Divider |
|---|---|
| PCIe-GenI (2.5 GHz) | 256 divisions |
| PCIe-GenII (5 GHz) | 512 divisions |

4. Adjust the rising or falling edge of the waveform to the Center of the sampling scope, and set the span to 5 ns/Div. Trace overlapping waveforms for 10 seconds with the oscilloscope, and check that the phase deviation of the measured waveforms falls within the range shown in Fig. 6.3.5-2.

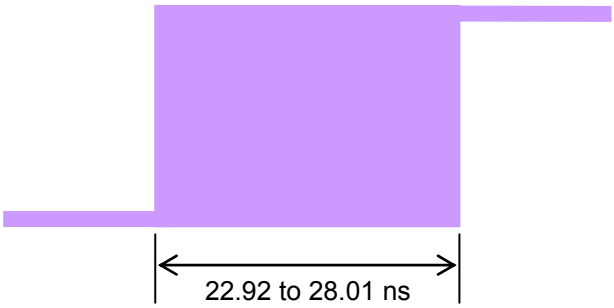



**Fig. 6.3.5-2 Example of waveform measured by sampling oscilloscope**

## 6.3.6 Measuring trigger output waveform

Measure the trigger output waveform. Connection with the measuring instrument is shown in Fig. 6.3.6-1.

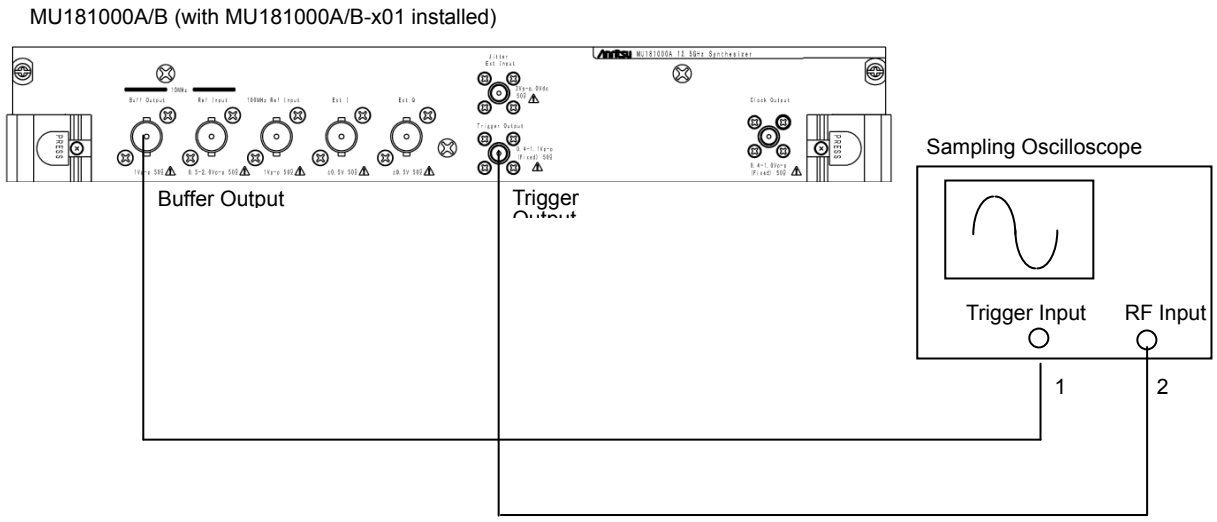



**Fig. 6.3.6-1 Connection diagram for trigger output waveform measurement**

1. Connect the Buff Output signal to the Trigger Input connector of the sampling oscilloscope.
2. Connect the Trigger Output signal to the RF Input connector of the sampling oscilloscope.

**⚠ CAUTION**

**Use an appropriate attenuator for each input connector when connecting to the input connector of the spectrum analyzer. Improper connection may damage the spectrum analyzer.**

3. Set as follows in the MU181000A/B operation window:
   Operation: Variable
   Center Frequency: See Table 6.3.6-1.
   Offset: 0 ppm
   Reference Clock Source: Internal
   Trigger Source: See Table 6.3.6-1.

**Table 6.3.6-1 Setting for trigger output waveform measurement**

| Center Frequency | Trigger Source |
|---|---|
| 12500 MHz | F/1 |
| 10000 MHz | F/1 |
| 6410 MHz | F/1 |
| 12500 MHz | F/64 |
| 10000 MHz | F/64 |
| 6400 MHz | F/64 |
| 3200 MHz | F/64 |
| 1600 MHz | F/64 |
| 810 MHz | F/64 |

4. Use the averaging function of the sampling oscilloscope to measure the waveform, and then check that the level of the measured waveform falls within the range shown Fig. 6.3.6-2.

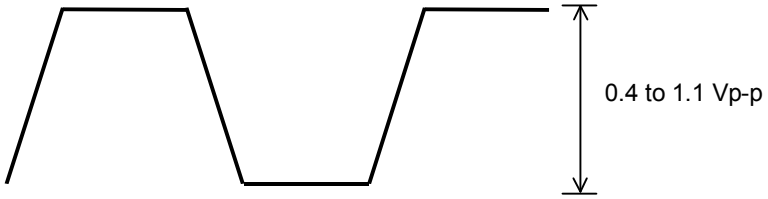



**Fig. 6.3.6-2 Example of waveform measured by sampling oscilloscope**

# Section 7 Maintenance

This section describes maintenance of the MU181000A/B.

# 7.1 Daily Maintenance

- Wipe off any external stains with a cloth damped with diluted mild detergent.
- Vacuum away any accumulated dust or dirt with a vacuum cleaner.
- Tighten any loose parts fixed with screws, using the specified tools.

# 7.2 Cautions on Storage

Wipe off any dust, soil, or stain on the MU181000A/B prior to storage. Avoid storing the MU181000A/B in any of the following locations:

- Where there is direct sunlight
- Where there is dust
- Where humidity is high and dew may accumulate
- Where chemically active gases are present
- Where the MU181000A/B may become oxidized
- Where strong vibrations are present
- Under the following temperature and humidity conditions:
  Temperature range of ≤–20°C or ≥60°C
  Humidity range of ≥85%

### Recommended storage conditions

In addition to the abovementioned storage cautions, the following environment conditions are recommended for long-term storage.

- Temperature range of 5 to 30°C
- Humidity range of 40 to 75%
- Slight daily fluctuation in temperature and humidity

# 7.3 Transportation

Use the original packing materials, if possible, when packing the MU181000A/B for transport. If you do not have the original packing materials, pack the MU181000A/B according to the following procedure. When handling the MU181000A/B, always wear clean gloves, and handle it gently so as not to damage it.

<Procedure>

1. Use a dry cloth to wipe off any stain or dust on the exterior of the MU181000A/B.
2. Check for loose or missing screws.
3. Provide protection for structural protrusions and parts that can easily be deformed, and wrap the MU181000A/B with a sheet of polyethylene. Finally, cover with moisture-proof paper.
4. Place the wrapped MU181000A/B into a cardboard box, and tape the flaps with adhesive tape. Furthermore, store it in a wooden box as required by the transportation distance or method.
5. During transportation, place it under an environment that meets the conditions described in Section 7.2 “Cautions on Storage”.

# 7.4 Calibration

Regular maintenance such as periodic inspections and calibration is essential for the Signal Quality Analyzer Series for long-term stable performance. Regular inspection and calibration are recommended for using the Signal Quality Analyzer Series in its prime condition at all times. The recommended calibration cycle after delivery of the Signal Quality Analyzer Series is twelve months.

If you require support after delivery, contact an Anritsu Service and Sales office. Contact information can be found on the last page of the printed version of this manual, and is available in a separate file on the CD version.

We may not provide calibration or repair if any of the following cases apply.

- Seven or more years have elapsed after production and parts for the instrument are difficult to obtain, or it is determined that reliability cannot be maintained after calibration/repair due to significant wear.
- Circuit changes, repair, or modifications are done without our approval.
- It is determined that the repair cost would be higher than the price of a new item.

# 7.5 Disposal

Confirm the notes described in the Signal Quality Analyzer Series Installation Guide and observe national and local regulations when disposing of the MU181000A/B.

# Section 8 Troubleshooting

This section describes how to check whether a failure has arisen when an error occurs during the operation of the MU181000A/B.

# 8.1 Problems Discovered during Module Replacement

**Table 8.1-1 Remedies for problems discovered during replacement of MU181000A/B**

| Symptom | Location to Check | Remedy |
|---|---|---|
| A module is not recognized. | Is the module installed properly? | Install the module again by referring to Section 2.3 "Installing and Removing Modules" in the installation guide. |
| | Is the module supported by the main frame? | Check the supported modules and the software version of The MU181000A/B at our Web site (http://www.anritsu.co.jp/E/MP1800A). If the module is supported, it may have failed. Contact an Anritsu Service and Sales office. Contact information can be found on the last page of the printed version of this manual, and is available in a separate file on the CD version. |

# 8.2 Problems Discovered during Use of MU181000A/B

**Table 8.2-1 Remedies for problems discovered during use of MU181000A/B**

| Symptom | Location to Check | Remedy |
|---|---|---|
| Output waveform is defective | Is the cable loose? | Tighten the connector. |
| | Do the cables used have good high-frequency characteristics? | Use cables and connectors with good high-frequency characteristics. |
| | Is the input clock signal used within the specification range? | Connect a signal that meets the input specifications for Clock Input. |
| | Is the measurement system for waveforms set as shown in Section 6.3 "Performance Test Item?" | Check the performance test procedure again. |

If a problem cannot be solved using any of the items listed above, perform initialization and check the items again. If the problem still occurs, contact an Anritsu Service and Sales office. Contact information can be found on the last page of the printed version of this manual, and is available in a separate file on the CD version.

# Appendix

# Appendix A Performance Test Result Sheet

# A.1 Performance Test Result Sheet

## A.1.1 Performance Test Result Sheet for MU181000A

Device name: MU181000A 12.5 GHz Synthesizer

Serial No.: _____

Ambient temperature: _____ °C

Relative humidity: _____ %

**Table A.1.1-1 Results of waveform measurement at Clock Output**

| Frequency [MHz] | Specifications | Measured Results |
|---|---|---|
| 12500 | 0.632 to 2.0 Vp-p | |
| 10000 | | |
| 5000 | | |
| 1000 | | |
| 100 | | |

**Table A.1.1-2 Results of phase noise measurement at Clock Output**

| Frequency [MHz] | Specifications | Measured Results | | |
|---|---|---|---|---|
| | | 1 kHz Offset | 10 kHz Offset | 100 kHz Offset |
| 12500 | ≤–61 dBc/Hz at 1 kHz offset | | | |
| 10000 | ≤–81 dBc/Hz at 10 kHz offset | | | |
| 5000 | ≤–90 dBc/Hz at 100 kHz offset | | | |
| 1000 | | | | |
| 100 | | | | |

Device name: MU181000A 12.5 GHz Synthesizer
(with MU181000A-x01 installed)

Serial No.: _____

Ambient temperature: _____℃

Relative humidity: _____%

**Table A.1.1-3 Results of waveform measurement at Clock Output**

| Frequency [MHz] | Specifications | Measured Results |
|---|---|---|
| 12500 | 0.4 to 1 Vp-p | |
| 10000 | | |
| 5000 | | |
| 1000 | | |
| 100 | | |

**Table A.1.1-4 Results of phase noise measurement at Clock Output**

| Frequency [MHz] | Specifications | Measured Results | | |
|---|---|---|---|---|
| | | 1 kHz Offset | 10 kHz Offset | 100 kHz Offset |
| 12500 | ≤–61 dBc/Hz at 1 kHz offset | | | |
| 10000 | ≤–81 dBc/Hz at 10 kHz offset | | | |
| 5000 | ≤–90 dBc/Hz at 100 kHz offset | | | |
| 1000 | | | | |
| 100 | | | | |

**Table A.1.1-5 Results of internal jitter accuracy measurement**

| Output Frequency [MHz] | Modulation Frequency [kHz] | Jitter Amplitude [UIp-p] | Specifications | Measured Results |
|---|---|---|---|---|
| 12500 | 0.009 | 4000 | 3718 to 4282 UIp-p | |
| | 0.22 | 4000 | 3718 to 4282 UIp-p | |
| | 1 | 880 | 816.4 to 943.6 UIp-p | |
| | 10 | 88 | 79.84 to 96.16 UIp-p | |
| | 100 | 8.8 | 79.84 to 96.16 UIp-p | |
| | 1000 | 0.88 | 0.754 to 1.006 UIp-p | |
| | 4000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 10000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 20000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 50000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 80000 | 0.22 | 0.167 to 0.273 UIp-p | |
| 9953.28 | 0.01 | 22 | 18.46 to 25.54 UIp-p | |
| | 40 | 22 | 18.46 to 25.54 UIp-p | |
| | 4000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 10000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 80000 | 0.22 | 0.167 to 0.273 UIp-p | |
| 6400 | 0.09 | 4000 | 3718 to 4282 UIp-p | |
| | 0.11 | 4000 | 3718 to 4282 UIp-p | |
| | 1 | 440 | 407.2 to 472.8 UIp-p | |
| | 10 | 44 | 38.92 to 40.08 UIp-p | |
| | 100 | 4.4 | 389.2 to 400.8 UIp-p | |
| | 1000 | 0.44 | 0.367 to 0.513 UIp-p | |
| | 2000 | 0.22 | 0.174 to 0.266 UIp-p | |
| | 5000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 10000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 20000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 40000 | 0.22 | 0.167 to 0.273 UIp-p | |
| 3200 | 0.009 | 4000 | 3718 to 4282 UIp-p | |
| | 0.055 | 4000 | 3718 to 4282 UIp-p | |
| | 0.1 | 2200 | 2044 to 2356 UIp-p | |
| | 1 | 220 | 202.6 to 4282 UIp-p | |
| | 10 | 22 | 18.46 to 25.54 UIp-p | |
| | 100 | 2.2 | 1.846 to 2.554 UIp-p | |
| | 1000 | 0.22 | 0.174 to 0.266 UIp-p | |
| | 5000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 10000 | 0.22 | 0.167 to 0.273 UIp-p | |

**Table A.1.1-5 Results of internal jitter accuracy measurement (Cont'd)**

| Output Frequency [MHz] | Modulation Frequency [kHz] | Jitter Amplitude [UIp-p] | Specifications | Measured Results |
|---|---|---|---|---|
| 2488.32 | 0.01 | 22 | 20.26 to 23.74 UIp-p | |
| | 10 | 22 | 20.26 to 23.74 UIp-p | |
| | 1000 | 0.22 | 0.174 to 0.266 UIp-p | |
| | 10000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 20000 | 0.22 | 0.167 to 0.273 UIp-p | |
| 1600 | 0.009 | 4000 | 3718 to 4282 UIp-p | |
| | 0.0275 | 4000 | 3718 to 4282 UIp-p | |
| | 0.1 | 1100 | 1021 to 1179 UIp-p | |
| | 1 | 110 | 100.3 to 119.7 UIp-p | |
| | 10 | 11 | 10.03 to 11.97 UIp-p | |
| | 100 | 1.1 | 1.003 to 1.197 UIp-p | |
| | 500 | 0.22 | 0.185 to 0.255 UIp-p | |
| | 1000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 2000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 5000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 10000 | 0.22 | 0.167 to 0.273 UIp-p | |
| 1244.16 | 0.01 | 22 | 18.46 to 25.54 UIp-p | |
| | 5 | 22 | 18.46 to 25.54 UIp-p | |
| | 500 | 0.22 | 0.185 to 0.255 UIp-p | |
| | 10000 | 0.22 | 0.167 to 0.273 UIp-p | |
| 800 | 0.009 | 4000 | 3718 to 4282 UIp-p | |
| | 0.01375 | 4000 | 3718 to 4282 UIp-p | |
| | 0.1 | 550 | 509.5 to 590.5 UIp-p | |
| | 1 | 55 | 49.15 to 60.85 UIp-p | |
| | 10 | 5.5 | 4.915 to 6.085 UIp-p | |
| | 100 | 0.55 | 0.502 to 0.599 UIp-p | |
| | 250 | 0.22 | 0.195 to 0.245 UIp-p | |
| | 1000 | 0.22 | 0.184 to 0.256 UIp-p | |
| | 2000 | 0.22 | 0.184 to 0.256 UIp-p | |
| | 5000 | 0.22 | 0.177 to 0.263 UIp-p | |
| 622.08 | 0.01 | 22 | 18.46 to 25.54 UIp-p | |
| | 2.5 | 22 | 18.46 to 25.54 UIp-p | |
| | 250 | 0.22 | 0.195 to 0.245 UIp-p | |
| | 5000 | 0.22 | 0.177 to 0.263 UIp-p | |

**Table A.1.1-5 Results of internal jitter accuracy measurement (Cont'd)**

| Output Frequency [MHz] | Modulation Frequency [kHz] | Jitter Amplitude [UIp-p] | Specifications | Measured Results |
|---|---|---|---|---|
| 155.52 | 0.01 | 22 | 18.46 to 25.54 UIp-p | |
| | 2.5 | 22 | 18.46 to 25.54 UIp-p | |
| | 250 | 0.22 | 0.195 to 0.245 UIp-p | |
| | 5000 | 0.22 | 0.177 to 0.263 UIp-p | |
| 100 | 0.01 | 22 | 18.46 to 25.54 UIp-p | |
| | 0.2 | 22 | 18.46 to 25.54 UIp-p | |
| | 20 | 0.22 | 0.195 to 0.245 UIp-p | |
| | 1000 | 0.22 | 0.184 to 0.256 UIp-p | |
| | 5000 | 0.22 | 0.177 to 0.263 UIp-p | |

**Table A.1.1-6 Results of external jitter accuracy measurement**

| Output Frequency [MHz] | Frequency Range | Amplitude Range | Input Frequency | Specifications | Measured Results |
|---|---|---|---|---|---|
| 12500 | 4.4 kHz | 4000 UI | 220 Hz | 1000 UIp-p ± 300 UI | |
| | 44 kHz | 200 UI | 4.4 kHz | 100 UIp-p ± 30 UI | |
| | 440 kHz | 20 UI | 44 kHz | 10 UIp-p ± 3 UI | |
| | 4 MHz | 2 UI | 440 kHz | 1 UIp-p ± 0.3 UI | |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 80 MHz | 0.22 UI | 80 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 500 MHz | 0.22 UI | 500 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 1 GHz | 0.22 UI | 1 GHz | 0.1 UIp-p ± 0.02 UI | |
| 6400 | 2.2 kHz | 4000 UI | 110 Hz | 1000 UIp-p ± 300 UI | |
| | 22 kHz | 200 UI | 2.2 kHz | 100 UIp-p ± 30 UI | |
| | 220 kHz | 20 UI | 22 kHz | 10 UIp-p ± 3 UI | |
| | 2 MHz | 2 UI | 220 kHz | 1 UIp-p ± 0.3 UI | |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 80 MHz | 0.22 UI | 80 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 500 MHz | 0.22 UI | 500 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 1 GHz | 0.22 UI | 1 GHz | 0.1 UIp-p ± 0.02 UI | |
| 3200 | 1.1 kHz | 4000 UI | 55 Hz | 1000 UIp-p ± 300 UI | |
| | 11 kHz | 200 UI | 1.1 kHz | 100 UIp-p ± 30 UI | |

**Table A.1.1-6 Results of external jitter accuracy measurement (Cont'd)**

| Output Frequency [MHz] | Frequency Range | Amplitude Range | Input Frequency | Specifications | Measured Results |
|---|---|---|---|---|---|
| 3200 | 110 kHz | 20 UI | 11 kHz | 10 UIp-p ± 3 UI | |
| | 1 MHz | 2 UI | 110 kHz | 1 UIp-p ± 0.3 UI | |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 80 MHz | 0.22 UI | 80 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 500 MHz | 0.22 UI | 500 MHz | 0.1 UIp-p ± 0.02 UI | |
| 1600 | 550 Hz | 4000 UI | 27.5 Hz | 1000 UIp-p ± 300 UI | |
| | 5.5 kHz | 200 UI | 550 Hz | 100 UIp-p ± 30 UI | |
| | 55 kHz | 20 UI | 5.5 kHz | 10 UIp-p ± 3 UI | |
| | 500 kHz | 2 UI | 55 kHz | 1 UIp-p ± 0.3 UI | |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 80 MHz | 0.22 UI | 80 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 500 MHz | 0.22 UI | 100 MHz | 0.1 UIp-p ± 0.02 UI | |
| 800 | 275 Hz | 4000 UI | 13.75 Hz | 1000 UIp-p ± 300 UI | |
| | 2.75 kHz | 200 UI | 275 Hz | 100 UIp-p ± 30 UI | |
| | 27.5 kHz | 20 UI | 2.75 kHz | 10 UIp-p ± 3 UI | |
| | 250 kHz | 2 UI | 27.5 Hz | 1 UIp-p ± 0.3 UI | |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 80 MHz | 0.22 UI | 20 MHz | 0.1 UIp-p ± 0.02 UI | |
| 100 | 275 Hz | 4000 UI | 13.75 Hz | 1000 UIp-p ± 300 UI | |
| | 2.75 kHz | 200 UI | 275 Hz | 100 UIp-p ± 30 UI | |
| | 27.5 kHz | 20 UI | 2.75 kHz | 10 UIp-p ± 3 UI | |
| | 250 kHz | 2 UI | 27.5 Hz | 1 UIp-p ± 0.3 UI | |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 80 MHz | 0.22 UI | 5 MHz | 0.1 UIp-p ± 0.02 UI | |

**Table A.1.1-7 Results of phase deviation measurement of triangular wave modulation**

| Output Freqneucy [MHz] | Specifications | Measured Results |
|---|---|---|
| 5000 | 18.00 to 22.00 ns | |
| 2500 | 18.00 to 22.00 ns | |

**Table A.1.1-8 Results of trigger output waveform measurement**

| Output Frequency [MHz] | Trigger Source | Specifications | Measured Results |
|---|---|---|---|
| 12500 | F/1 | 0.4 to 1.1 Vp-p | |
| 10000 | F/1 | | |
| 6410 | F/1 | | |
| 12500 | F/64 | | |
| 10000 | F/64 | | |
| 6400 | F/64 | | |
| 3200 | F/64 | | |
| 1600 | F/64 | | |
| 810 | F/64 | | |

## A.1.2 Performance Test Result Sheet for MU181000B

Device name: MU181000B 12.5 GHz 4 port Synthesizer

Serial No.: _____

Ambient temperature: _____℃

Relative humidity: _____%

**Table A.1.2-1 Results of waveform measurement at Clock Output**

| Frequency [MHz] | Specifications | Measured Results |
|---|---|---|
| 12500 | 0.632 to 2.0 Vp-p | |
| 10000 | | |
| 5000 | | |
| 1000 | | |
| 100 | | |

**Table A.1.2-2 Results of phase noise measurement at Clock Output**

| Frequency [MHz] | Specifications | Measured Results | | |
|---|---|---|---|---|
| | | 1 kHz Offset | 10 kHz Offset | 100 kHz Offset |
| 12500 | ≤–61 dBc/Hz at 1 kHz offset | | | |
| 10000 | ≤–81 dBc/Hz at 10 kHz offset | | | |
| 5000 | ≤–90 dBc/Hz at 100 kHz offset | | | |
| 1000 | | | | |
| 100 | | | | |

Device name: MU181000B 12.5 GHz 4 port Synthesizer
(with MU181000B-x01 installed)

Serial No.: _____

Ambient temperature: _____℃

Relative humidity: _____%

**Table A.1.2-3 Results of waveform measurement at Clock Output**

| Frequency [MHz] | Specifications | Measured Results |
|---|---|---|
| 12500 | 0.4 to 1 Vp-p | |
| 10000 | | |
| 5000 | | |
| 1000 | | |
| 100 | | |

**Table A.1.2-4 Results of phase noise measurement at Clock Output**

| Frequency [MHz] | Specifications | Measured Results | | |
|---|---|---|---|---|
| | | 1 kHz Offset | 10 kHz Offset | 100 kHz Offset |
| 12500 | ≤–61 dBc/Hz at 1 kHz offset | | | |
| 10000 | ≤–81 dBc/Hz at 10 kHz offset | | | |
| 5000 | ≤–90 dBc/Hz at 100 kHz offset | | | |
| 1000 | | | | |
| 100 | | | | |

**Table A.1.2-5 Results of internal jitter accuracy measurement**

| Output Frequency [MHz] | Modulation Frequency [kHz] | Jitter Amplitude [UIp-p] | Specifications | Measured Results |
|---|---|---|---|---|
| 12500 | 0.009 | 4000 | 3718 to 4282 UIp-p | |
| | 0.22 | 4000 | 3718 to 4282 UIp-p | |
| | 1 | 880 | 816.4 to 943.6 UIp-p | |
| | 10 | 88 | 79.84 to 96.16 UIp-p | |
| | 100 | 8.8 | 79.84 to 96.16 UIp-p | |
| | 1000 | 0.88 | 0.754 to 1.006 UIp-p | |
| | 4000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 10000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 20000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 50000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 80000 | 0.22 | 0.167 to 0.273 UIp-p | |
| 9953.28 | 0.01 | 22 | 18.46 to 25.54 UIp-p | |
| | 40 | 22 | 18.46 to 25.54 UIp-p | |
| | 4000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 10000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 80000 | 0.22 | 0.167 to 0.273 UIp-p | |
| 6400 | 0.09 | 4000 | 3718 to 4282 UIp-p | |
| | 0.11 | 4000 | 3718 to 4282 UIp-p | |
| | 1 | 440 | 407.2 to 472.8 UIp-p | |
| | 10 | 44 | 38.92 to 40.08 UIp-p | |
| | 100 | 4.4 | 389.2 to 400.8 UIp-p | |
| | 1000 | 0.44 | 0.367 to 0.513 UIp-p | |
| | 2000 | 0.22 | 0.174 to 0.266 UIp-p | |
| | 5000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 10000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 20000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 40000 | 0.22 | 0.167 to 0.273 UIp-p | |
| 3200 | 0.009 | 4000 | 3718 to 4282 UIp-p | |
| | 0.055 | 4000 | 3718 to 4282 UIp-p | |
| | 0.1 | 2200 | 2044 to 2356 UIp-p | |
| | 1 | 220 | 202.6 to 4282 UIp-p | |
| | 10 | 22 | 18.46 to 25.54 UIp-p | |
| | 100 | 2.2 | 1.846 to 2.554 UIp-p | |
| | 1000 | 0.22 | 0.174 to 0.266 UIp-p | |
| | 5000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 10000 | 0.22 | 0.167 to 0.273 UIp-p | |

**Table A.1.2-5 Results of internal jitter accuracy measurement (Cont'd)**

| Output Frequency [MHz] | Modulation Frequency [kHz] | Jitter Amplitude [UIp-p] | Specifications | Measured Results |
|---|---|---|---|---|
| 2488.32 | 0.01 | 22 | 20.26 to 23.74 UIp-p | |
| | 10 | 22 | 20.26 to 23.74 UIp-p | |
| | 1000 | 0.22 | 0.174 to 0.266 UIp-p | |
| | 10000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 20000 | 0.22 | 0.167 to 0.273 UIp-p | |
| 1600 | 0.009 | 4000 | 3718 to 4282 UIp-p | |
| | 0.0275 | 4000 | 3718 to 4282 UIp-p | |
| | 0.1 | 1100 | 1021 to 1179 UIp-p | |
| | 1 | 110 | 100.3 to 119.7 UIp-p | |
| | 10 | 11 | 10.03 to 11.97 UIp-p | |
| | 100 | 1.1 | 1.003 to 1.197 UIp-p | |
| | 500 | 0.22 | 0.185 to 0.255 UIp-p | |
| | 1000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 2000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 5000 | 0.22 | 0.167 to 0.273 UIp-p | |
| | 10000 | 0.22 | 0.167 to 0.273 UIp-p | |
| 1244.16 | 0.01 | 22 | 18.46 to 25.54 UIp-p | |
| | 5 | 22 | 18.46 to 25.54 UIp-p | |
| | 500 | 0.22 | 0.185 to 0.255 UIp-p | |
| | 10000 | 0.22 | 0.167 to 0.273 UIp-p | |
| 800 | 0.009 | 4000 | 3718 to 4282 UIp-p | |
| | 0.01375 | 4000 | 3718 to 4282 UIp-p | |
| | 0.1 | 550 | 509.5 to 590.5 UIp-p | |
| | 1 | 55 | 49.15 to 60.85 UIp-p | |
| | 10 | 5.5 | 4.915 to 6.085 UIp-p | |
| | 100 | 0.55 | 0.502 to 0.599 UIp-p | |
| | 250 | 0.22 | 0.195 to 0.245 UIp-p | |
| | 1000 | 0.22 | 0.184 to 0.256 UIp-p | |
| | 2000 | 0.22 | 0.184 to 0.256 UIp-p | |
| | 5000 | 0.22 | 0.177 to 0.263 UIp-p | |
| 622.08 | 0.01 | 22 | 18.46 to 25.54 UIp-p | |
| | 2.5 | 22 | 18.46 to 25.54 UIp-p | |
| | 250 | 0.22 | 0.195 to 0.245 UIp-p | |
| | 5000 | 0.22 | 0.177 to 0.263 UIp-p | |

**Table A.1.2-5 Results of internal jitter accuracy measurement (Cont'd)**

| Output Frequency [MHz] | Modulation Frequency [kHz] | Jitter Amplitude [UIp-p] | Specifications | Measured Results |
|---|---|---|---|---|
| 155.52 | 0.01 | 22 | 18.46 to 25.54 UIp-p | |
| | 2.5 | 22 | 18.46 to 25.54 UIp-p | |
| | 250 | 0.22 | 0.195 to 0.245 UIp-p | |
| | 5000 | 0.22 | 0.177 to 0.263 UIp-p | |
| 100 | 0.01 | 22 | 18.46 to 25.54 UIp-p | |
| | 0.2 | 22 | 18.46 to 25.54 UIp-p | |
| | 20 | 0.22 | 0.195 to 0.245 UIp-p | |
| | 1000 | 0.22 | 0.184 to 0.256 UIp-p | |
| | 5000 | 0.22 | 0.177 to 0.263 UIp-p | |

**Table A.1.2-6 Results of external jitter accuracy measurement**

| Output Frequency [MHz] | Frequency Range | Amplitude Range | Input Frequency | Specifications | Measured Results |
|---|---|---|---|---|---|
| 12500 | 4.4 kHz | 4000 UI | 220 Hz | 1000 UIp-p ± 300 UI | |
| | 44 kHz | 200 UI | 4.4 kHz | 100 UIp-p ± 30 UI | |
| | 440 kHz | 20 UI | 44 kHz | 10 UIp-p ± 3 UI | |
| | 4 MHz | 2 UI | 440 kHz | 1 UIp-p ± 0.3 UI | |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 80 MHz | 0.22 UI | 80 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 500 MHz | 0.22 UI | 500 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 1 GHz | 0.22 UI | 1 GHz | 0.1 UIp-p ± 0.02 UI | |
| 6400 | 2.2 kHz | 4000 UI | 110 Hz | 1000 UIp-p ± 300 UI | |
| | 22 kHz | 200 UI | 2.2 kHz | 100 UIp-p ± 30 UI | |
| | 220 kHz | 20 UI | 22 kHz | 10 UIp-p ± 3 UI | |
| | 2 MHz | 2 UI | 220 kHz | 1 UIp-p ± 0.3 UI | |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 80 MHz | 0.22 UI | 80 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 500 MHz | 0.22 UI | 500 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 1 GHz | 0.22 UI | 1 GHz | 0.1 UIp-p ± 0.02 UI | |
| 3200 | 1.1 kHz | 4000 UI | 55 Hz | 1000 UIp-p ± 300 UI | |
| | 11 kHz | 200 UI | 1.1 kHz | 100 UIp-p ± 30 UI | |

**Table A.1.2-6 Results of external jitter accuracy measurement (Cont'd)**

| Output Frequency [MHz] | Frequency Range | Amplitude Range | Input Frequency | Specifications | Measured Results |
|---|---|---|---|---|---|
| 3200 | 110 kHz | 20 UI | 11 kHz | 10 UIp-p ± 3 UI | |
| | 1 MHz | 2 UI | 110 kHz | 1 UIp-p ± 0.3 UI | |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 80 MHz | 0.22 UI | 80 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 500 MHz | 0.22 UI | 500 MHz | 0.1 UIp-p ± 0.02 UI | |
| 1600 | 550 Hz | 4000 UI | 27.5 Hz | 1000 UIp-p ± 300 UI | |
| | 5.5 kHz | 200 UI | 550 Hz | 100 UIp-p ± 30 UI | |
| | 55 kHz | 20 UI | 5.5 kHz | 10 UIp-p ± 3 UI | |
| | 500 kHz | 2 UI | 55 kHz | 1 UIp-p ± 0.3 UI | |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 80 MHz | 0.22 UI | 80 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 500 MHz | 0.22 UI | 100 MHz | 0.1 UIp-p ± 0.02 UI | |
| 800 | 275 Hz | 4000 UI | 13.75 Hz | 1000 UIp-p ± 300 UI | |
| | 2.75 kHz | 200 UI | 275 Hz | 100 UIp-p ± 30 UI | |
| | 27.5 kHz | 20 UI | 2.75 kHz | 10 UIp-p ± 3 UI | |
| | 250 kHz | 2 UI | 27.5 Hz | 1 UIp-p ± 0.3 UI | |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 80 MHz | 0.22 UI | 20 MHz | 0.1 UIp-p ± 0.02 UI | |
| 100 | 275 Hz | 4000 UI | 13.75 Hz | 1000 UIp-p ± 300 UI | |
| | 2.75 kHz | 200 UI | 275 Hz | 100 UIp-p ± 30 UI | |
| | 27.5 kHz | 20 UI | 2.75 kHz | 10 UIp-p ± 3 UI | |
| | 250 kHz | 2 UI | 27.5 Hz | 1 UIp-p ± 0.3 UI | |
| | 4 MHz | 0.22 UI | 4 MHz | 0.1 UIp-p ± 0.02 UI | |
| | 80 MHz | 0.22 UI | 5 MHz | 0.1 UIp-p ± 0.02 UI | |

**Table A.1.2-7 Results of phase deviation measurement of triangular wave modulation**

| Output Freqneucy [MHz] | Specifications | Measured Results |
|---|---|---|
| 5000 | 18.00 to 22.00 ns | |
| 2500 | 18.00 to 22.00 ns | |

**Table A.1.2-8 Results of trigger output waveform measurement**

| Output Frequency [MHz] | Trigger Source | Specifications | Measured Results |
|---|---|---|---|
| 12500 | F/1 | 0.4 to 1.1 Vp-p | |
| 10000 | F/1 | | |
| 6410 | F/1 | | |
| 12500 | F/64 | | |
| 10000 | F/64 | | |
| 6400 | F/64 | | |
| 3200 | F/64 | | |
| 1600 | F/64 | | |
| 810 | F/64 | | |

# B.1 List of Initial Setting Items

**Table B.1-1 Initial setting items for MU181000A/B**

| Item | | Value |
|---|---|---|
| Operation Frequency | Center Frequency | 12500 MHz |
| | Offset | 0 |
| Reference Clock | Source | Internal |

**Table B.1-2 Initial setting items when MU181000A/B-x01 is installed**

| Item | | Value |
|---|---|---|
| Operation Frequency | Operation | Variable |
| | Center Frequency | 12500 MHz |
| | Offset | 0 |
| Reference Clock | Source | Internal |
| Spectrum Spread | SSC | Nondisplay |
| | Spread Method | Nondisplay |
| Jitter | Jitter | OFF |
| | Modulation Source | Internal |
| | Frequency | 9.000 Hz |
| | Amplitude | 1.000 UIp-p |
| | Trigger Source | f/64 |

# *Appendix C List of Setting Items in Customize Screen*

## C.1 List of Setting Items in Customize Screen

**Table C.1-1 Setting items in customize screen**

| Item | |
|---|---|
| Operation Frequency | Center Frequency (in the selected units) |
| | Offset |